

ERC12864-655 Series

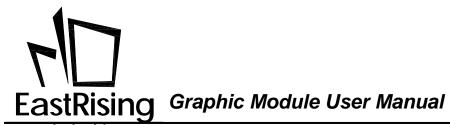
Graphic Module User Manual

EastRising Technology Co., Limited

Attention:

- A. Click" CONTENTS" list could be directed to the detail page. Click "Return to CONTENTS" could be directed to CONTENTS Page.
- B. Some specifications of IC are not listed in this manual. Please refer to the IC manual for more details.
- C. The drawing for related touch panel, schematic drawing, demo code is all available, please contact with our sales if you need.
- D. Please pay more attention to "INSPECTION CRITERIA" in this manual. We assume you already agree with these criterions when you place an order with us. No more recommendations.

REV	DESCRIPTION	RELEASE DATE
1.0	Preliminary Release	Jun-11-2012



ERC12864-655 Series

www.lcd-china.com

CONTENTS

1. ORDERING INFORMATION	04
	04 05
2. ATTRIBUTES	06
2.2 Mechanical Attribute	06 06 06 06
3. OUTLINE DRAWING	07
4. ELECTRICAL SPEC	08
	80
	10 10
5. INSPECTION CRITERIA	11
	11
	11
	11 12
	14
	15
6. PRECAUTIONS FOR USING	17
	17
	18
	18 1 0
	18 18
	18



EastRising Graphic Module User Manual ERC12864-655 Series

www.lcd-china.com	
7. USING LCD MODULES	 19
7.1 Liquid Crystal Display Modules	 19
7.2 Installing LCD Modules	 19
7.3 Precaution for Handling LCD Modules	 20
7.4 Electro-Static Discharge Control	 20
7.5 Precaution for Soldering to EastRising LCM	 20
7.6 Precaution for Operation	 21
7.7 Limited Warranty	 21
7.8 Return Policy	 21



www.lcd-china.com

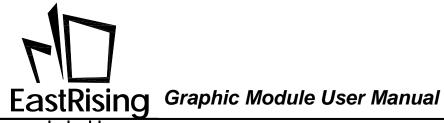
1. ORDERING INFORMATION

1.1 ERC12864-655 Series Table

>>Return to CONTENTS

*The number of series table is in accordance with number of the below series image 1.2.

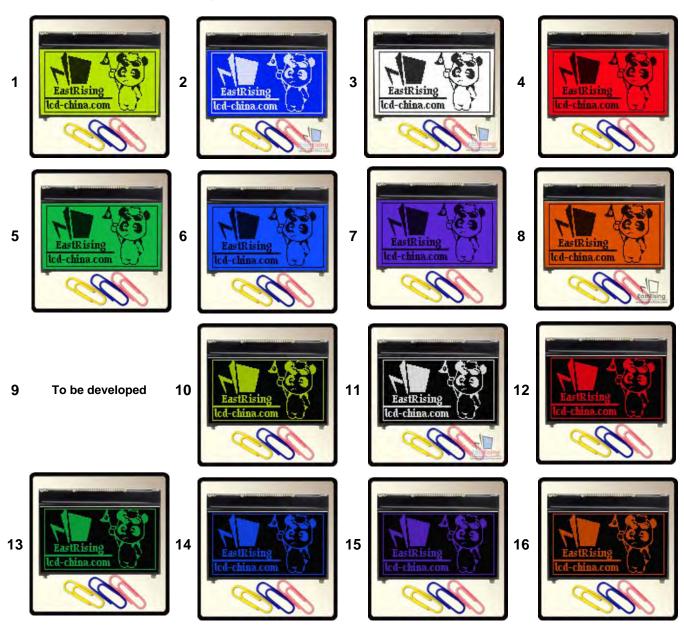
No.	Part Number	LCD Type	Backlight Color	Graphic & Font Color	Background Color
1	ERC12864FYGF-655	FSTN Positive	Yellow Green Color	Dark Blue	Yellow Green Color
2	ERC12864SBSF-655	STN Negative Blue	White Color	White Color	Blue Color
3	ERC12864FSF-655	FSTN Positive	White Color	Black Color	White Color
4	ERC12864FRF-655	FSTN Positive	Red Color	Black Color	Red Color
5	ERC12864FGF-655	FSTN Positive	Green Color	Black Color	Green Color
6	ERC12864FBF-655	FSTN Positive	Blue Color	Black Color	Blue Color
7	ERC12864FPF-655	FSTN Positive	Purple Color	Black Color	Purple Color
8	ERC12864FAMF-655	FSTN Positive	Amber Color	Black Color	Amber Color
9	ERC12864F7-655	FSTN Positive	RGB Color	Black Color	RGB Color
10	ERC12864DNYGF-655	FFSTN Negative	Yellow Green Color	Yellow Green Color	Black Color
11	ERC12864DNSF-655	FFSTN Negative	White Color	White Color	Black Color
12	ERC12864DNRF-655	FFSTN Negative	Red Color	Red Color	Black Color
13	ERC12864DNGF-655	FFSTN Negative	Green Color	Green Color	Black Color
14	ERC12864DNBF-655	FFSTN Negative	Blue Color	Blue Color	Black Color
15	ERC12864DNPF-655	FFSTN Negative	Purple Color	Purple Color	Black Color
16	ERC12864DNAMF-655	FFSTN Negative	Amber Color	Amber Color	Black Color
17	ERC12864DN7-655	FFSTN Negative	RGB Color	RGB Color	Black Color



1.2 ERC12864-655 Series Image

>>Return to CONTENTS

*The number of series image is in accordance with number of the above series table 1.1.



17 To be developed



Graphic Module User Manual ERC12864-655 Series

www.lcd-china.com

2. ATTRIBUTES

2.1 Display Attributes

>>Return to CONTENTS

ITEM	STANDARD VALUE	UNIT
Dot Matrix	128 x 64 Dots	
Display Connector	Metal PIN	
FPC Connector	1.27mm Pitch 22 Pins	
Operating Temperature	-20 ~ +70	°C
Storage Temperature	-30 ~ +80	°C
Touch Panel Optional	N/A	
Font Chip Optional	N/A	
*Sunlight Readable	No1,No3,No4,No5,No6,No7,No8,No9	

*Number of sunlight readable is from 1.1 ERC12864-655 Series Table of the manual.

2.2 Mechanical Attributes

2.2 Mechanical Attributes >>Return to CONTENT		
ITEM	STANDARD VALUE	UNIT
Outline Dimension with FPC Folded	70.0(W) × 50.0(H) × 5.8(T) (MAX)	mm
Visual Area	67.0(W) × 37.0(H)	mm
Active Area	63.95(W) × 31.95(H)	mm
Dot Size	0.45 × 0.45	mm
Dot Pitch	0.50 × 0.50	mm
Net Weight	31.0 ± 15% grams (typical)	g

2.3 Electrical Attributes

ITEM	STANDARD VALUE	UNIT
IC Package	COG	
Controller	UC1701	
Interface	8080 8-bit Parallel, 6800 8-bit Parallel,4-Wire SPI	

2.4 Optical Attributes

>>Return to CONTENTS

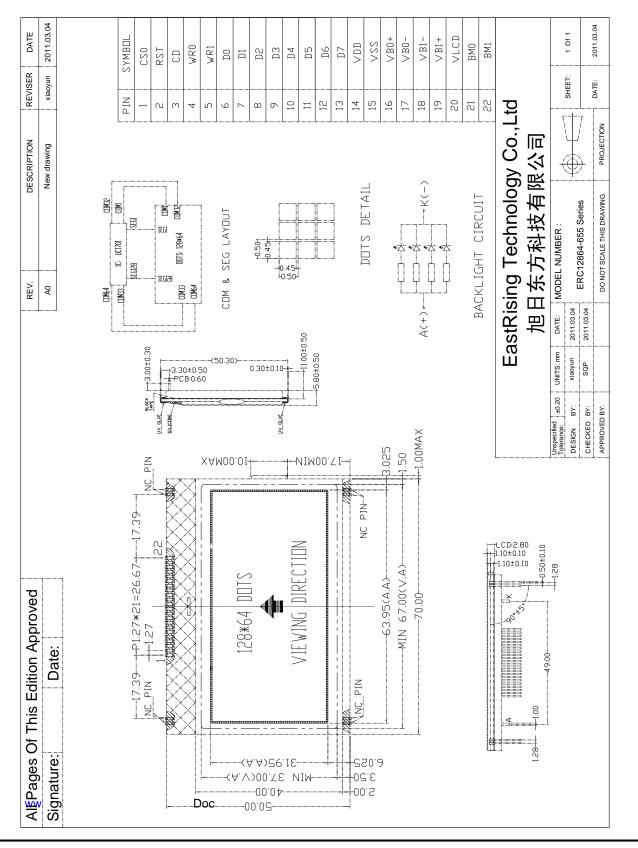
ITEM	STANDARD VALUE	UNIT
LCD Type	Refer to 1.1 ERC12864-655 Series Table	
Backlight Color	Refer to1.1 ERC12864-655 Series Table	
Viewing Direction	6:00	Clock
LCD Duty	1/64	Duty
LCD Bias	1/9	Bias



ERC12864-655 Series

www.lcd-china.com

3. OUTLINE DRAWING



East

4. ELECTRICAL SPEC

isina

4.1 Pin Configuration

Name	Descript	ions								
/CS0	Chip Sele	Chip Select. Chip is selected when CS0="L". When the chip is not selected,								
	D[7:0] wi	ll be of hi	gh impeda	nce.						
RST	When RS	ST=L, all	control reg	isters a	re re-init	ialized by	/ their de	fault state	es.	
	When RS	ST is not	used, conr	ect the	pin to V	DD				
	Select Co	ontrol dat	a or Displa	iy data	for read/	write ope	eration.			
CD		•								
	"L" : Con	trol data								
	WR[1:0]	controls t	he read/wi	ite ope	ration of	the host	interface			
WR0				-						
						odes, the	se two pi	ns are no	ot	
	,									
				•						
WR1				-				nterface it is		
						odes, the	se two pi	ns are no	ot	
	,									
					•		erface.			
	In serial i		_] to SD			1			
				D5					D0	
D0~D7	BM=1	DB7	DB6	DB	DB4	DB3	DB2	DB1	DB0	
	Х			5						
	BM=0	SDA	SDK	-	-	-	-	-	-	
	Х									
	-		•	to eith	er VSS o	r VDD				
	Negative	Power S	supply, Gro	und(0V	')					
-										
_	LCD Bias	s Voltage	S.							
_										
BM0					is detern	nined by	BM[1:0] a	and {D7,	D6}	
BM1	by the fo	llowing re	lationship:							
	RST CD WR0 WR1 WR1 D0~D7 VDD VSS VB0+ VB0- VB1- VB1- VB1+ VLCD BM0	D[7:0] wi When RS When RS When RS When RS When RS CDCDSelect Co "H": Disp "L": Con "L": Con WR[1:0] In paralle in, 6800 used, con used, con Used, con In paralle in, 6800 used, con Bi-directi In serial nWR1Bi-directi In serial nD0~D7Bi-directi In serial nD0~D7BM=1 x BM=0 xVDDPositive NVDDPositive NVB0+ VB0+ VB0- VB1+LCD Bias NegativeVLCDMain LCI BM0BM0Bus mod	D[7:0] will be of hiRSTWhen RST=L, all When RST is notCDSelect Control data "H": Display data "L": Control dataWR0WR[1:0] controls to In parallel mode, to in, 6800 or 8080 r used, connect theWR1WR[1:0] controls to In parallel mode, to in, 6800 or 8080 r used, connect theWR1Bi-directional bus In serial modes, co D0~D7D0~D7BM=1 BM=1 BM=1 DB7 x D87 D84 D7VDDPositive Power Su VSSVB0+ VB0+ VB0- US1+LCD Bias Voltage VB1+VLCDMain LCD Power BM0BM0Bus mode: The in	D[7:0] will be of high impedaRSTWhen RST=L, all control reg When RST is not used, commodely Select Control data or Display "H" : Display data "L" : Control dataCD"H" : Display data "L" : Control dataWR0WR[1:0] controls the read/wr In parallel mode, the meanin in, 6800 or 8080 mode. In sec used, connect them to VSS of WR1WR1WR[1:0] controls the read/wr In parallel mode, the meanin in, 6800 or 8080 mode. In sec used, connect them to VSS of Bi-directional bus for both sec In serial modes, connect D[7D0~D7Bi-directional bus for both sec In serial modes, connect D[7D0~D7BM=1D8DB7D8D8VDDPositive Power SupplyVSSNegative Power SupplyVB0+ VB0- VB1+LCD Bias Voltages.VLCDMain LCD Power Supply.BM0Bus mode: The interface bus	D[7:0] will be of high impedance.RSTWhen RST=L, all control registers a When RST is not used, connect the Select Control data or Display data "H" : Display data "L" : Control dataCD"H" : Display data "L" : Control dataWR0In parallel mode, the read/write ope In parallel mode, the meaning of WI in, 6800 or 8080 mode. In serial inte used, connect them to VSS or VDDWR1WR[1:0] controls the read/write ope In parallel mode, the meaning of WI in, 6800 or 8080 mode. In serial inte used, connect them to VSS or VDDWR1Bi-directional bus for both serial and In serial modes, connect D[7] to SDD0-D7Bi-directional bus for both serial and In serial modes, connect D[7] to SDD0-D7BM=1DB7BM=1DB7DB6VDDPositive Power SupplyVSSNegative Power SupplyVB0+ VB0-LCD Bias Voltages.VB1+VLCDMain LCD Power Supply.BM0Bus mode: The interface bus mode	D[7:0] will be of high impedance.RSTWhen RST=L, all control registers are re-init When RST is not used, connect the pin to VI Select Control data or Display data for read/ "H" : Display data "L" : Control dataCD"H" : Display data "L" : Control dataWR0In parallel mode, the read/write operation of In parallel mode, the meaning of WR[1:0] de in, 6800 or 8080 mode. In serial interface modused, connect them to VSS or VDD.WR1WR[1:0] controls the read/write operation of In parallel mode, the meaning of WR[1:0] de in, 6800 or 8080 mode. In serial interface modused, connect them to VSS or VDD.WR1Bi-directional bus for both serial and parallel In serial modes, connect D[7] to SDA, D[6] to Do-D7Bi-directional bus for both serial and parallel In serial modes, connect D[7] to SDA, D[6] to Always connect unused pins to either VSS or VDDVDDPositive Power SupplyVSSNegative Power Supply, Ground(0V)VB0+ VB0- LCD Bias Voltages.VB1+WR0BM0Bus mode: The interface bus mode is determined	D[7:0] will be of high impedance. RST When RST=L, all control registers are re-initialized by When RST is not used, connect the pin to VDD CD Select Control data or Display data for read/write operation of the host In parallel mode, the meaning of WR[1:0] depends or in, 6800 or 8080 mode. In serial interface modes, the used, connect them to VSS or VDD. WR1 WR[1:0] controls the read/write operation of the host In parallel mode, the meaning of WR[1:0] depends or in, 6800 or 8080 mode. In serial interface modes, the used, connect them to VSS or VDD. WR1 WR[1:0] controls the read/write operation of the host In parallel mode, the meaning of WR[1:0] depends or in, 6800 or 8080 mode. In serial interface modes, the used, connect them to VSS or VDD. WR1 In parallel mode, the meaning of WR[1:0] depends or in, 6800 or 8080 mode. In serial and parallel host interface modes, the used, connect them to VSS or VDD. D0~D7 Bi-directional bus for both serial and parallel host interface modes, connect D[7] to SDA, D[6] to SCK. D0~D7 BM=1 DB7 DB6 DB DB4 DB3 x 5 -	D[7:0] will be of high impedance. RST When RST=L, all control registers are re-initialized by their der When RST is not used, connect the pin to VDD CD "H" : Display data "L" : Control data or Display data for read/write operation. "H" : Display data "L" : Control data WR0 WR[1:0] controls the read/write operation of the host interface. In parallel mode, the meaning of WR[1:0] depends on which ir in, 6800 or 8080 mode. In serial interface modes, these two pi used, connect them to VSS or VDD. WR1 WR[1:0] controls the read/write operation of the host interface. In parallel mode, the meaning of WR[1:0] depends on which ir in, 6800 or 8080 mode. In serial interface modes, these two pi used, connect them to VSS or VDD. WR1 Bi-directional bus for both serial and parallel host interface. In serial modes, connect D[7] to SDA, D[6] to SCK. Do-D7 BM=1 DB7 D6 D5 D4 D3 D2 BM=0 SDA SDK - - - Always connect unused pins to either VSS or VDD VDD Positive Power Supply. VSS VB0+ VSS Negative Power Supply, Ground(0V) VB0+ VB0+ VB0+ VED Bias Voltages. VB1+ VLCD Main LCD Power Supply.	D[7:0] will be of high impedance. RST When RST=L, all control registers are re-initialized by their default state When RST is not used, connect the pin to VDD Select Control data or Display data for read/write operation. "H" : Display data "L" : Control data WR0 "H": Display data "L": Control data WR[1:0] controls the read/write operation of the host interface. In parallel mode, the meaning of WR[1:0] depends on which interface it in, 6800 or 8080 mode. In serial interface modes, these two pins are not used, connect them to VSS or VDD. WR1 WR[1:0] controls the read/write operation of the host interface. In parallel mode, the meaning of WR[1:0] depends on which interface it in, 6800 or 8080 mode. In serial interface modes, these two pins are not used, connect them to VSS or VDD. WR1 Bi-directional bus for both serial and parallel host interface. In serial modes, connect D[7] to SDA, D[6] to SCK. D0-D7 BM=1 D87 D86 D8 D84 D83 D82 D81 x 5 - - - - - - VDD Positive Power Supply VSS Negative Power Supply VS Negative Power Supply, Ground(0V) VB0+ VB0+ VB0- LCD Bias Voltages. VB1+ VLCD Main LCD Power Supply. BM0 Bus mode: The interface	

ERC12864-655 Series

www.lcd-china.com

sina

Ea

ou orminar				
	11		Data	6800/8-bit
	10)	Data	8080/8-bit
	0x		SDA,SCK	4-wire SPI w/8-bit token
				(S8: conversional)

Graphic Module User Manual ERC12864-655 Series sina

www.lcdchina.com

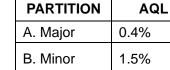
Eas

4.2 Absolute Maximum Ratings

4.2 Absolute Maximum Ratings >>Return to CONTENTS					
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Power Supply for Logic	VDD-VSS	-0.3	-	+4.0	V
Power Supply for LCD	VOUT	-0.3	-	+13.2	V
Input Voltage	VIN	-0.4	-	Vdd+0.3	V
Supply Current for Backlight	ILED	-	-	100	mA

4.3 Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply for LCM	VDD-VSS	-	3.0	3.3	3.6	V
Input Voltage	VIL	L Level	VSS	-	0.2VDD	V
	VIH	H Level	0.8VDD	-	VDD	V
LCD Driving Voltage	Vop-VSS	-	9.0	9.2	9.4	V
Supply Current for LCM	IDD	VDD=3.3V	-	-	500.0	uA
Supply Current for Backlight	ILED	-	40	60	80	mA
Power Supply for Backlight	VLED	-	-	3.3	-	V
(White,Blue,Green Color)						
Power Supply for Backlight	VLED	-	-	3.3	-	V
(Red,Purple,Amber Color)						
Power Supply for Backlight	VLED	-	-	3.3	-	V
(Yellow Green Color)						



5.2 Definition of Lot

One lot means the delivery quantity to customer at one time.

Functional defective as product

Each lot should satisfy the quality level defined as follows

5.3 Condition of Cosmetic Inspection

5. INSPECTION CRITERIA

5.1 Acceptable Quality Level

- ♦ INSPECTION AND TEST
 - -FUNCTION TEST -APPEARANCE INSPECTION
 - -PACKING SPECIFICTION
- ♦ INSPECTION CONDITION
 - Put under the lamp (20wiÁ2) at a distance 100mm from
 - Tilt upright 45 degree by the front (back) to inspect LCD appearance.
- ♦ AQL INSPECTION LEVEL
 - SAMPLING METHOD: MIL-STD-105D
 - SAMPLING PLAN: SINGLE
 - MAJOR DEFECT: 0.65% (MAJOR)
 - MINOR DEFECT: 2.5% (MINOR)
 - GENERAL LEVEL: II/NORMAL

Graphic Module User Manual ERC12864-655 Series

DEFINITION

>>Return to CONTENTS

Satisfy all functions as product but not satisfy cosmetic stanard

>>Return to CONTENTS

EastRising

www.lcd-china.com

ERC12864-655 Series

www.lcd-china.com

EastRising

5.4 Module Cosmetic Criteria

NO.	Item	Judgment Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern Peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on substrate	Invisible copper foil ($@0.5$ mm or more) on substrate pattern	Minor
5	Accretion of metallic	No soldering dust	Minor
	Foreign matter	No accretion of metallic foreign matters(Not exceed ¢ 0.2mm)	
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor
8	Solder amount	a. Soldering side of PCB	Minor
	1.Lead parts	Solder to form a'Filet' all around the lead. Solder should not hide the lead form perfectly.(too much) b.Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB	
	2.Flat packages	Either 'toe'(A) or 'heal' (B) of the lead to be covered by 'Filet' Lead form to be assume over Solder.	Minor
	3.Chips	(3/2) H≥h≥(1/2)H	Minor



ERC12864-655 Series

www.lcd-china.com

<u></u>			
9	Backlight defects	 Light fails or flickers.(Major) Color and luminance do not correspond to specifications. (Major) Exceeds standards for display's blemishes, foreign matter, dark lines or scratches.(Minor) 	See list ←
10	PCB defects	Oxidation or contamination on connectors.* 2. Wrong parts, missing parts, or parts not in specification.* 3.Jumpers set incorrectly.(Minor) 4.Solder(if any)on bezel,LED pad,zebra pad,or screw hole pad is not smooth.(Minor) *Minor if display functions correctly.Major if the display fails.	See list ←
11	Soldering defects	 Unmelted solder paste. Cold solder joints, missing solder connections, or oxidation.* Solder bridges causing short circuits.* Residue or solder balls. Solder flux is black or brown. *Minor if display functions correctly.Major if the display fails. 	Minor

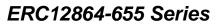


Graphic Module User Manual ERC12864-655 Series

5.5 Screen Cosmetic Criteria (Non-Operating) >>Return to COI				
No.	Defect	Judgment Criterion		Partition
1	Spots	In accordance with Screen Cosme	etic Criteria (Operating) No.1.	Minor
2	Lines	In accordance with Screen Cosme	etic Criteria (Operation) No.2.	Minor
3	Bubbles in			Minor
	Polarizer	Size: d mm	Acceptable Qty in active area	
		d≦0.3	Disregard	
		0.3 <d≦1.0< td=""><td>3</td><td></td></d≦1.0<>	3	
		1.0 <d≦1.5< td=""><td>1</td><td></td></d≦1.5<>	1	
		1.5 <d< td=""><td>0</td><td></td></d<>	0	
4	Scratch	In accordance with spots and lines operating cosmetic criteria, When the		Minor
		light reflects on the panel surface, the scratches are not to be remarkable.		
5	Allowable density	Above defects should be separated more than 30mm each other.		Minor
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels.		Minor
		Back-lit type should be judged with back-lit on state only.		
7	Contamination	Not to be noticeable.		Minor



Graphic Module User Manual



5.6 Screen Cosmetic Criteria (Operating)

>>Return to CONTENTS

No.	Defect	Judgmei	nt Criterion	Partition
1 S	Spots	A) Clear		Minor
		Size:d mm	Acceptable Qty in active area	
		d≦0.1	Disregard	
		0.1 <d≦0.2< td=""><td>6</td><td></td></d≦0.2<>	6	
		0.2 <d≦0.3< td=""><td>2</td><td></td></d≦0.3<>	2	
		0.3 <d< td=""><td>0</td><td></td></d<>	0	
		Note: Including pin holes and defecti	ive dots which must be within one pixel	
		Size.		
		B) Unclear		
		Size:d mm	Acceptable Qty in active area	-
		d≦0.2	Disregard	-
		0.2 <d≦0.5< td=""><td>6</td><td></td></d≦0.5<>	6	
		0.5 <d≦0.7< td=""><td>2</td><td></td></d≦0.7<>	2	
		0.7 <d< td=""><td>0</td><td></td></d<>	0	
2 L	_ines	A) Clear		Minor
		L 5.0 2.0 (6) (0) (6) (6) (6) Note: () – Acceptable Qty in active a L - Length (mm) W -Width(mm) ∞ -Disregard B) Unclear L 10.0 ∞ (6) (6	See No.1 W 	

'Unclear' = The shade and size are changed by Vo.

No.	Defect	Judgment Criterion	Partition



G Graphic Module User Manual

ww	www.lcd-china.com			
3	Rubbing line	Not to be noticeable.		
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor	
5	Rainbow	Not to be noticeable.	Minor	
6	Dot size	To be 95%~105% of the dot size (Typ.) in drawing.	Minor	
		Partial defects of each dot (ex.pin-hole) should be treated as'spot'.		
		(see Screen Cosmetic Criteria (Operating) No.1)		
7	Brightness	Brightness Uniformity must be BMAX/BMIN≦2	Minor	
	(only back-lit	- BMAX :Max.value by measure in 5 points		
	Module)	- BMIN : Min.value by measure in 5 points		
		Divide active area into 4 vertically and horizontally.		
		Measure 5 points shown in the following figure.		
8	Contrast	Contrast Uniformity must be BmAX/BMIN≦2	Minor	
	Uniformity	Measure 5 points shown in the following figure.		
		Dashed lines divide active area into 4 vertically and horizontally.		
		Measuring points are located at the inter-sections of dashed line.		
		Note: BMAX – Max.value by measure in 5 points.		
		BMIN – Min.value by measure in 5 points.		
		O - Measuring points in $ $		

Note:

(1) Size : d=(long length + short length)/2

(2) The limit samples for each item have priority.

(3) Complexed defects are defined item by item, but if the number of defects is defined in above table, the total number should not exceed 10.



(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not be allowed. Following three situations

Should be treated as 'concentration'.

- -10 or over defects in circle of $\not\subset$ 10mm
- -20 or over defects in circle of $\not\subset$ 20mm

6. PRECAUTIONS FOR USING

6.1 Handling Precautions

- This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.
- EastRising display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.
- If EastRising display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the EastRising display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the EastRising display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- If EastRising display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following Isopropyl or alcohol.
- Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the Water.
- Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- Install the EastRising LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the cable or the backlight cable.
- Do not attempt to disassemble or process EastRising LCD module.
- NC terminal should be open. Do not connect anything.
- If the logic circuit power is off, do not apply the input signals.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - -Be sure to ground the body when handling EastRising LCD modules.
 - -Tools required for assembling, such as soldering irons, must be properly grounded.
 - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

6.2 Power Supply Precautions

Risina

• Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is some variance between models.

- Prevent the application of reverse polarity to VDD and VSS, however briefly.
- Use a clean power source free from transients. Power-up conditions are occasionally jolting and may exceed the maximum ratings of EastRising modules.
- ◆ The VDD power of EastRising module should also supply the power to all devices that may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.

6.3 Operating Precautions

- DO NOT plug or unplug EastRising module when the system is powered up.
- Minimize the cable length between EastRising module and host MPU.
- ◆ For models with backlights, do not disable the backlight by interrupting the HV line. Unload inverters produce voltage extremes that may arc within a cable or at the display.
- Operate EastRising module within the limits of the modules temperature specifications.

6.4 Mechanical/Environmental Precautions

- ◆ Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the electrometric connection and cause display failure.
- Mount EastRising module so that it is free from torque and mechanical stress.
- Surface of the LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- Always employ anti-static procedure while handling EastRising module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage tem
- Do not store in direct sunlight
- ◆ If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap

6.5 Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep EastRising modules in bags (avoid high temperature / high humidity and low temperatures below 0C Whenever possible, EastRising LCD modules should be stored in the same conditions in which they were shipped from our company.

6.6 Others

>>Return to CONTENTS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module

>>Return to CONTENTS

>>Return to CONTENTS

>>Return to CONTENTS

>>Return to CONTENTS



Graphic Module User Manual ERC12864-655 Series



is subject to a low temperature.

If EastRising LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

-Exposed area of the printed circuit board.

-Terminal electrode sections.

7. USING LCD MODULES

- 7.1 Liquid Crystal Display Modules >>Return to CONTENTS EastRising LCD is composed of glass and polarizer. Pay attention to the following items when handling.
- ◆ Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- When EastRising display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- Avoid contacting oil and fats.
- Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- Do not put or attach anything on EastRising display area to avoid leaving marks on.
- Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

7.2 Installing LCD Modules

- Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ± 0.1 mm.

Document Name: ERC12864-655 Series Manual-Rev1.0

ERC12864-655 Series

URL: www.lcd-china.com

Graphic Module User Manual

www.lcd-china.com

- 7.3 Precaution for Handling LCD Modules >>Return to CONTENTS Since EastRising LCM has been assembled and adjusted with a high degree of precision; avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- ◆ Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist EastRising LCM.

7.4 Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- Make certain that you are grounded when handing LCM.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- ◆ As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

7.5 Precaution for Soldering to EastRising LCM

Observe the following when soldering lead wire, connector cable and etc. to the LCM. -Soldering iron temperature : $280^{\circ}C \pm 10^{\circ}C$

-Soldering time: 3-4 sec.

-Solder: eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

- When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

ERC12864-655 Series

>>Return to CONTENTS

>>Return to CONTENTS

EastRising

URL: www.lcd-china.com

www.lcd-china.com

EastRising

7.6 Precaution for Operation

• Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.

Graphic Module User Manual

- Driving the EastRising LCD in the voltage above the limit shortens its life.
- Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- If EastRising display area is pushed hard during operation, the display will become abnormal. However, it
 will return to normal if it is turned off and then back on.
- ◆ Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- When turning the power on, input each signal after the positive/negative voltage becomes stable.

7.7 Limited Warranty

Unless agreed between EastRising and customer, EastRising will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with EastRising LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to EastRising within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of EastRising limited to repair and/or replacement on the terms set forth above. EastRising will not be responsible for any subsequent or consequential events.

7.8 Return Policy

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

-Broken LCD glass.

-PCB eyelet damaged or modified.

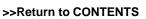
- -PCB conductors damaged.
- -Circuit modified in any way, including addition of components.
- -PCB tampered with by grinding, engraving or painting varnish.

-Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals

That's the end of the Manual

>>Return to CONTENTS



>>Return to CONTENTS

ERC12864-655 Series