



## OTA7001A

---

### 1200CH TFT LCD Source Driver with TCON

***Preliminary***

JUL. 07, 2010

Version 0.3

## Table of Contents

	<u>PAGE</u>
<b>1. GENERAL DESCRIPTION</b> .....	<b>3</b>
<b>2. FEATURES</b> .....	<b>3</b>
<b>3. BLOCK DIAGRAM</b> .....	<b>4</b>
<b>4. SIGNAL DESCRIPTIONS</b> .....	<b>5</b>
4.1. PAD DESCRIPTION .....	5
4.2. CASCADE SIGNAL DESCRIPTION .....	8
4.3. VALUE OF WIRING RESISTANCE TO EACH PIN .....	10
<b>5. FUNCTIONAL DESCRIPTIONS</b> .....	<b>11</b>
5.1. POWER ON/OFF SEQUENCE .....	11
5.2. DATA INPUT FORMAT .....	12
5.3. BIST FUNCTION .....	14
5.4. CABAC ARCHITECTURE .....	15
5.5. TCON TIMING .....	15
5.6. APPLICATION DIAGRAM .....	18
5.7. USEFUL & USELESS CHANNEL OF SOURCE DRIVER .....	19
5.8. INPUT DATA VS OUTPUT VOLTAGE .....	19
5.9. GAMMA CORRECTION RESISTOR RATIO (GMA_EN=1) .....	20
5.10. OUTPUT VOLTAGE VS. INPUT DATA (GMA_EN=1) .....	21
<b>6. ELECTRICAL SPECIFICATIONS</b> .....	<b>23</b>
6.1. ABSOLUTE MAXIMUM RATINGS .....	23
6.2. RECOMMENDED OPERATING CONDITIONS .....	23
6.3. DC CHARACTERISTICS .....	24
6.4. AC CHARACTERISTICS .....	25
<b>7. CHIP INFORMATION</b> .....	<b>29</b>
7.1. PAD ASSIGNMENT .....	29
7.2. PAD DIMENSION .....	29
7.3. PAD LOCATION .....	30
7.4. ALIGNMENT MARK .....	39
<b>8. DISCLAIMER</b> .....	<b>40</b>
<b>9. REVISION HISTORY</b> .....	<b>41</b>

---

## 1200CH TFT LCD Source Driver with TCON

### 1. GENERAL DESCRIPTION

OTA7001A is a highly integrated 1200 channel source driver with TTL interface Timing Controller for color TFT-LCD panels. OTA7001A integrated source driver, timing controller and pin control Interface.

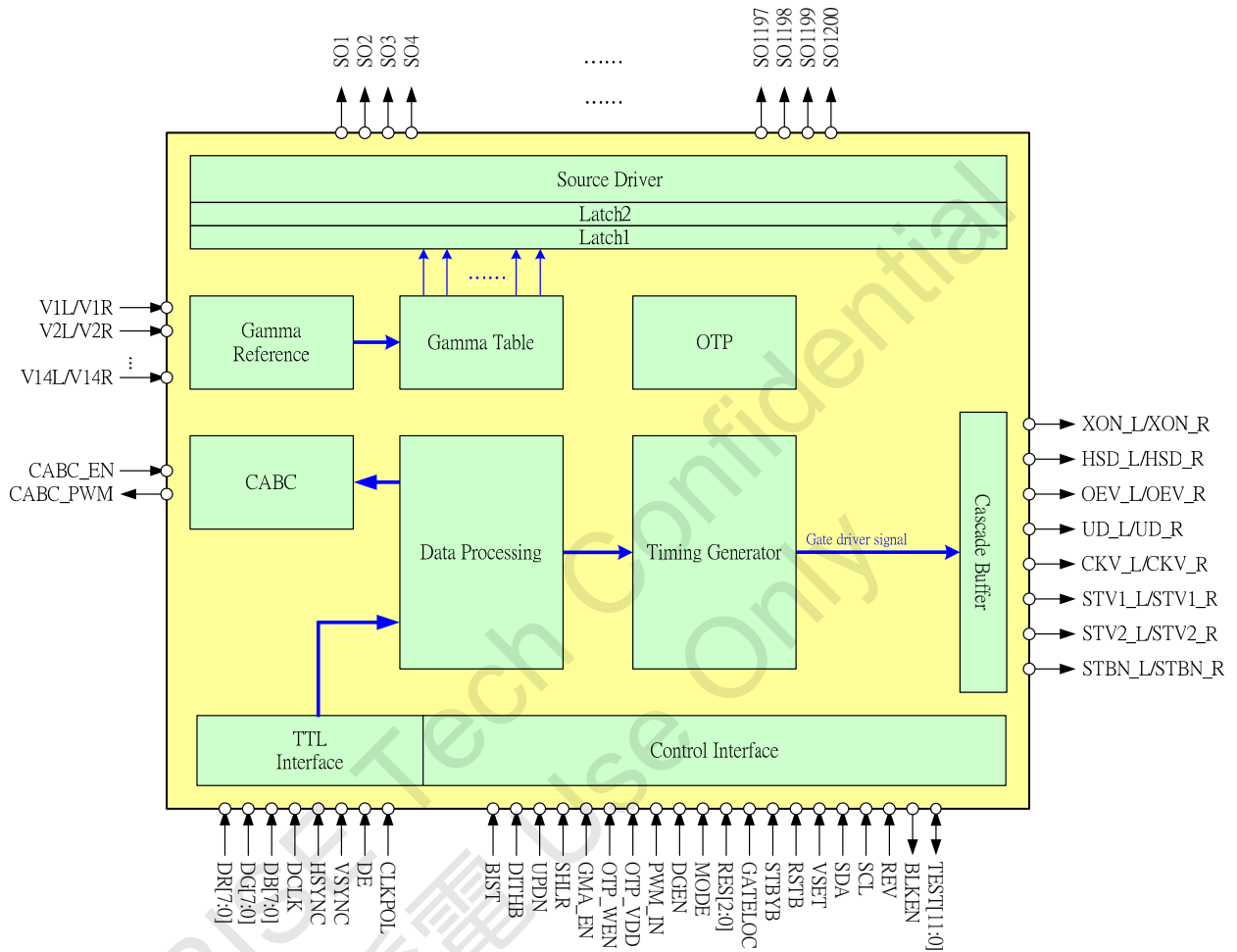
Input timing supports TTL digital 24bit parallel RGB data format. Source output support 8-bit resolution 256 grayscale with dithering features. Operating parameters can be set via OTP and pin control for all control features.

Special circuit architecture is designed for lower power dissipation. With wide range of supply voltages and small output deviations make this chip more suitable for various applications.

### 2. FEATURES

- Special design for medium-sized color TFT LCD source drivers with timing controller
- Integrated 1200 channel source driver
- Supports display resolutions : 800(RGB)x600,800(RGB)x480, 768(RGB)x1014,600(RGB)x800,600(RGB)x480,1024x768(Mono-TFT), 960x1014(Mono-TFT)
- 8-bit resolution 256 gray scale with 2-bits dithering
- Supports TTL 24-bit parallel (RGB) input timing
- Support cascade function with bidirectional shift control (CMOS signal)
- Support Dual Gate architecture
- Support stand-by mode for low power consumption
- Support dot inversion driving scheme
- Built-in OTP for internal parameter setting
- Built-in internal gamma and support external gamma buffer
- Support CABC function to reduce backlight unit power
- V1~V14 for adjusting Gamma correction
- Support 1-line and 1+2line polarity inversion
- Output max. dynamic range : 0.1 ~ VDDA-0.1v
- Max. power for source driver voltage VDDA : 13.5v
- power for digital interface circuit VDDD : 2.3v ~ 3.6v
- Operating frequency : 50MHz
- COG package

### 3. BLOCK DIAGRAM



## 4. SIGNAL DESCRIPTIONS

### 4.1. Pad Description

Designation	I/O	Description																			
SO1 ~ SO1200	O	Source Driver output.																			
V1L ~ V14L	I	External Gamma voltage input on left side. These voltages must provided by user.																			
V1R ~ V14R	I	External Gamma voltage input on right side. These voltages must provided by user.																			
XON_L	O	Cascade data signal for left side pad, gate driver signal output																			
XON_R	O	Cascade data signal for right side pad, gate driver signal output																			
HSD_L	O	Cascade data signal for left side pad, gate driver signal output																			
HSD_R	O	Cascade data signal for right side pad, gate driver signal output																			
OEV_L	O	Cascade data signal for left side pad, gate driver signal output																			
OEV_R	O	Cascade data signal for right side pad, gate driver signal output																			
UD_L	O	Cascade data signal for left side pad, gate driver signal output																			
UD_R	O	Cascade data signal for right side pad, gate driver signal output																			
CKV_L	O	Cascade data signal for left side pad, gate driver signal output																			
CKV_R	O	Cascade data signal for right side pad, gate driver signal output																			
STV1_L	O	Cascade data signal for left side pad, gate driver signal output																			
STV1_R	O	Cascade data signal for right side pad, gate driver signal output																			
STV2_L	O	Cascade data signal for left side pad, gate driver signal output																			
STV2_R	O	Cascade data signal for right side pad, gate driver signal output																			
STBN_L	O	Cascade data signal for left side pad, gate driver signal output																			
STBN_R	O	Cascade data signal for right side pad, gate driver signal output																			
REV	I,PL	Data inverted control. 1: Data inverted for normally black LCD 0: Data inverted for normally white LCD																			
SCL	I,PH	Multi function I/O pin. Note: Fix to the VDDD level if no used																			
SDA	I,PH	Multi function I/O pin. Note: Fix to the VDDD level if no used																			
VSET	I,PL	Gamma voltage input selection. 0: 10 gamma voltage input, V2, V6, V9, V13 is disable 1: V1~V14 gamma voltage input.																			
DCLK	I	Parallel RGB clock input																			
DR/DG/DB[7:0]	I	Parallel RGB R/G/B data input, 8-bits data input																			
HSYNC/VSYNC/DE	I	Parallel RGB HSYNC / VSYNC / DE input																			
CLKPOL	I,PL	Parallel RGB clock polarity control																			
RSTB	I,PH	External reset input, low active Suggest to connecting with an RC reset circuit for stability																			
STBYB	I,PH	External standby mode selection, low active																			
GATELOC	I,PL	Gate IC location selection, 1 : Gate IC on left (From panel top view); 0 : Gate IC on right (From panel top view)																			
RES[2:0]	I,	Resolution selection <table border="1" data-bbox="450 1697 970 1966"> <thead> <tr> <th>DGEN</th> <th>RES[2:0]</th> <th>Resolution</th> </tr> </thead> <tbody> <tr> <td rowspan="5">1</td> <td>0xx</td> <td>768x1024</td> </tr> <tr> <td>100</td> <td>800x480 (default)</td> </tr> <tr> <td>101</td> <td>800x600</td> </tr> <tr> <td>110</td> <td>600x480</td> </tr> <tr> <td>111</td> <td>600x800</td> </tr> <tr> <td rowspan="2">0</td> <td>xx0</td> <td>1024x768(Mono-TFT)</td> </tr> <tr> <td>xx1</td> <td>960x1024(Mono-TFT)</td> </tr> </tbody> </table>	DGEN	RES[2:0]	Resolution	1	0xx	768x1024	100	800x480 (default)	101	800x600	110	600x480	111	600x800	0	xx0	1024x768(Mono-TFT)	xx1	960x1024(Mono-TFT)
DGEN	RES[2:0]	Resolution																			
1	0xx	768x1024																			
	100	800x480 (default)																			
	101	800x600																			
	110	600x480																			
	111	600x800																			
0	xx0	1024x768(Mono-TFT)																			
	xx1	960x1024(Mono-TFT)																			
MODE	I,PH	DE/SYNC mode select. Normally pull high 0 : SYNC mode 1 : DE mode																			
DGEN	I,PH	Dual gate function enable / disable, 0 : Disable; 1 : Enable																			

Designation	I/O	Description
PWM_IN	I,PH	PWM input
OTP_VDD	P	OTP high voltage input during write, Connect to 6.75v when programming
OTP_WEN	I,PL	Set to logic "1" when OTP programming
GMA_EN	I,PL	External Gamma enable 0: Enable internal Gamma, Gamma input voltage generated by internal circuit 1: Disable internal Gamma, V1~V14 Gamma input voltage
SHLR	I,PH	Left / Right shift control, 0 : Shift left : S1200 → S1199 → ... → S2 → S1 1 : Shift right, S1 → S2 → S3 → ... → S1200
UPDN	I,PL	Up / Down rotate control 0 : STV2 output vertical start pulse and UD output logic "0" to gate driver 1 : STV1 output vertical start pulse and UD output logic "1" to gate driver
DITHB	I,PH	Dithering function enable / disable, 0 : Enable; 1 : Disable
BIST	I,PL	BIST Mode enable / disable, , 0 : Disable; 1 : Enable
CABC_EN	I,PL	CABC function enable / disable, 0 : Disable; 1 : Enable
CABC_PWM	O	PWM output after CABC function
BLKEN	O	Backlight enable signal
TEST[11:0]	I/O	Test mode selection, for MP testing usage
VDDA	P	Analog power, connect to +13.5v (max.)
VSSA	P	Analog ground
VDDD	P	Digital power input, range 2.3v ~ 3.6v
VSSD	P	Digital power ground
COM_PASSA COM_PASSB	S	Pass line for COM, internal link together between input side and output side
DUM_GND	SH	dummy pad, internal connect to ground.

**NOTE :**

I : Input ,O : Output, P : Power, D : Dummy, S : shorted line , M : mark, P : Power pad  
T : Testing, SH : shielding, I/O : Input / Output, PS : Power Setting, C : Capacitor pin.



## 4.2. Cascade Signal Description

### 4.2.1. Left Side Cascade Signals

	GATELOC=0	GATELOC=1
<i>Description</i>	Gate IC on right	Gate IC on left
XON_L	X	XON
HSD_L	X	HSD
OEV_L	X	OEV
UD_L	X	UD
CKV_L	X	CKV
STV1_L	X	STV1
STV2_L	X	STV2
STV1_L	X	STV1
STBN_L	X	STBN

ORISE Tech Confidential  
For 橋電 Use Only



## 4.2.2. Right Side Cascade Signals

	GATELOC=0	GATELOC=1
<i>Description</i>	Gate IC on right	Gate IC on left
XON_R	XON	X
HSD_R	HSD	X
OEV_R	OEV	X
UD_R	UD	X
CKV_R	CKV	X
STV1_R	STV1	X
STV2_R	STV2	X
STV1_R	STV1	X
STBN_R	STBN	X

ORISE Tech Confidential  
For 橋電 Use Only

#### 4.3. Value of wiring resistance to each pin

The recommended wiring resistance values are shown below. The wiring resistance values affect the current capacity of the power supply, so be sure to design using values that do not exceed those recommended.

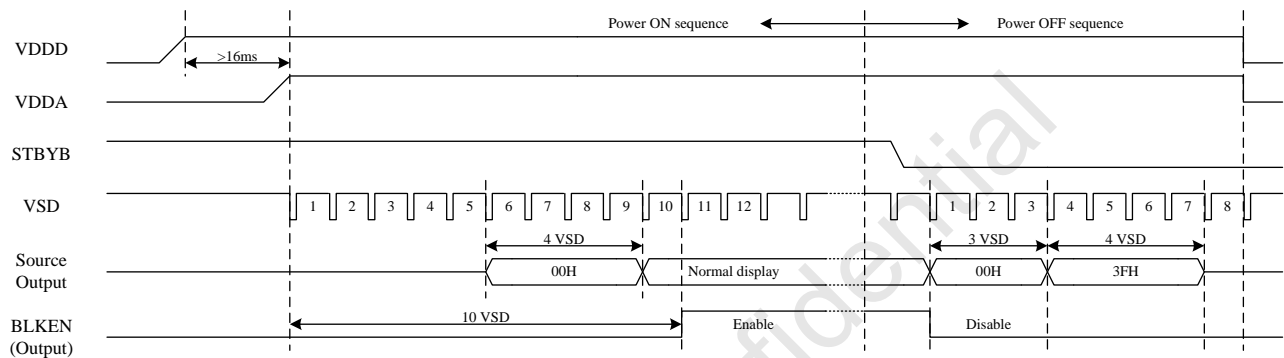
Pin Name	Wiring resistance value ( $\Omega$ )	Pin Name	Wiring resistance value ( $\Omega$ )
VDDD	<10	SDA	<200
VDDA	<5	SCL	<200
VSSD	<10	BLKEN	<200
VSSA	<5	CABC_PWM	<200
V1L~V14L V1R ~ V14R	<40	Cascade signals	<200 & 20pf
DR/DG/DB[7:0]	<50		
HSYNC/VSYNC/DE/DCLK	<50		
CLKPOL RSTB STBYB VSET REV GATELOC RES[2:0] SHLR UPDN DGEN GMA_EN CABC_EN MODE DITHB BIST CABC_EN	<1K		

## 5. FUNCTIONAL DESCRIPTIONS

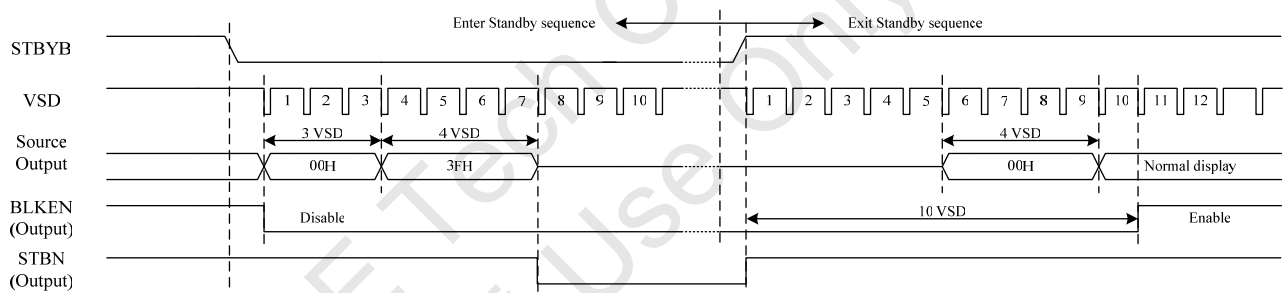
### 5.1. Power On/Off Sequence

In order to prevent IC from power on reset fail, the rising time ( $T_{POR}$ ) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

#### Power-On/Off Timing Sequence:

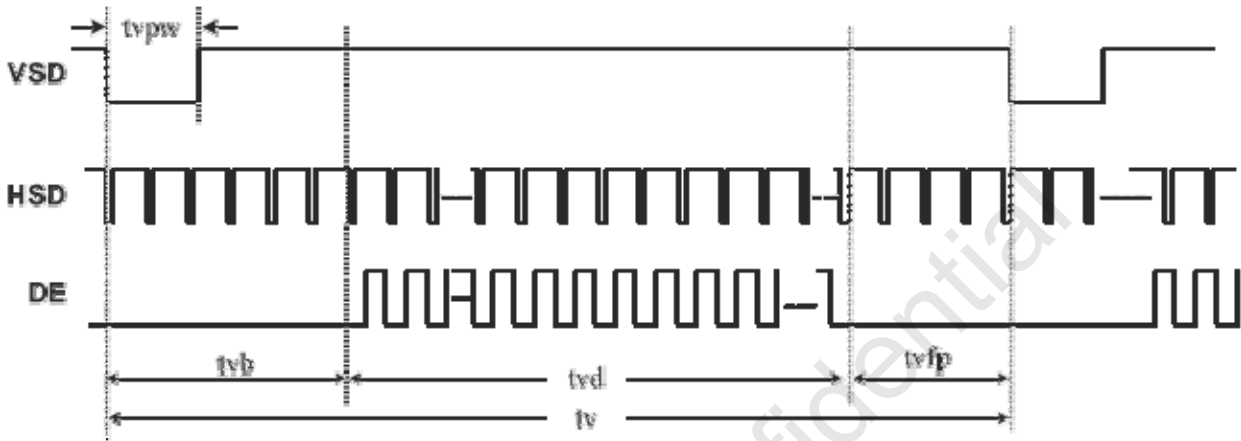


#### Enter and Exit Standby Mode Sequence:

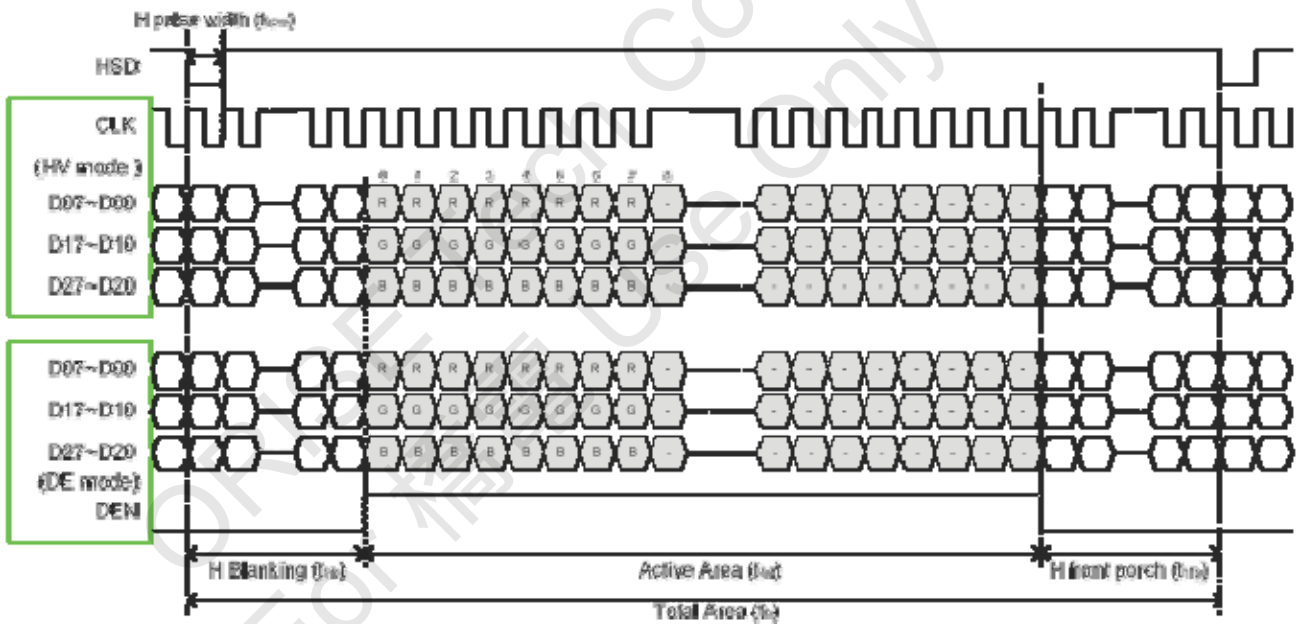


## 5.2. Data Input Format

### Vertical input timing



### Horizontal input timing



**Timing Characteristic**
**For 800x480 panel**

## Horizontal input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Horizontal display area	thd	800			DCLK
DCLK frequency	fclk	-	30	50	MHz
1 Horizontal Line	th	928			DCLK
HSD pulse width	thpw	Min.	1		
		Typ.	48		
		Max.	-		
HSD Back Porch (Blanking)	thb	-	88	-	
HSD Front Porch	thfp	-	40	-	

## Vertical input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	480			H
VSD period time	tv	-	525	-	H
VSD pulse width	tvpw	-	3	-	H
VSD Back Porch (Blanking)	tvb	-	32	-	H
VSD Front Porch	tvfp	-	13	-	H

**For 800x600 panel**

## Horizontal input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Horizontal display area	thd	800			DCLK
DCLK frequency	fclk	-	40	50	MHz
1 Horizontal Line	th	1000			DCLK
HSD pulse width	thpw	Min.	1		
		Typ.	48		
		Max.	-		
HSD Back Porch (Blanking)	thb	-	88	-	
HSD Front Porch	thfp	-	112	-	

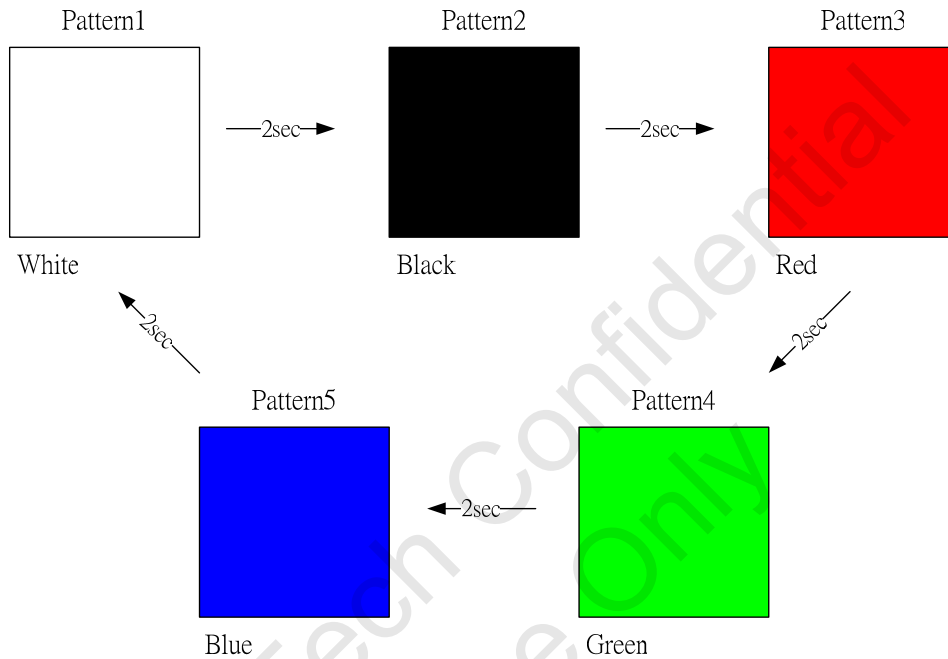
## Vertical input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSD period time	tv	-	660	-	H
VSD pulse width	tvpw	-	3	-	H
VSD Back Porch (Blanking)	tvb	-	39	-	H
VSD Front Porch	tvfp	-	21	-	H

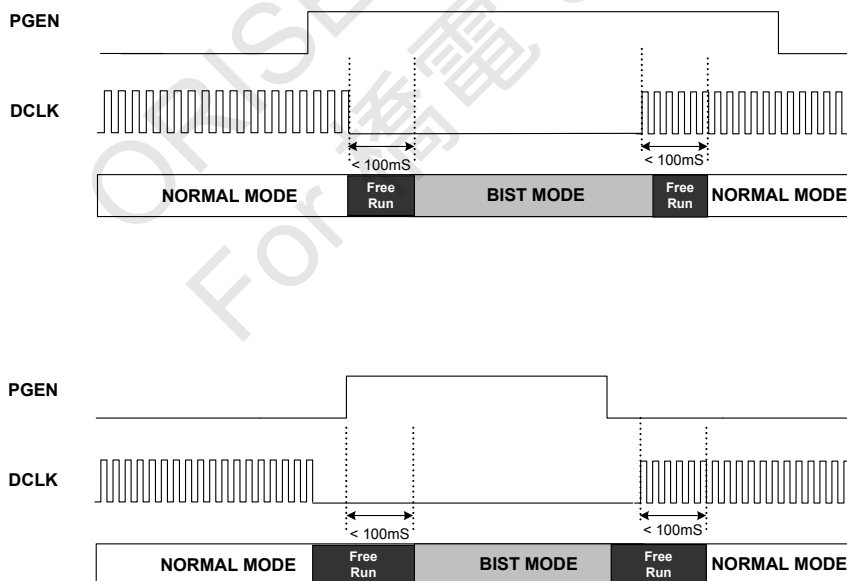
### 5.3. BIST function

When BIST = HIGH and no any input signals, the internal pattern generator activates and outputs the following self test patterns. These self test patterns show around 2 seconds.

#### 5.3.1. BIST pattern



#### 5.3.2. BIST Timing

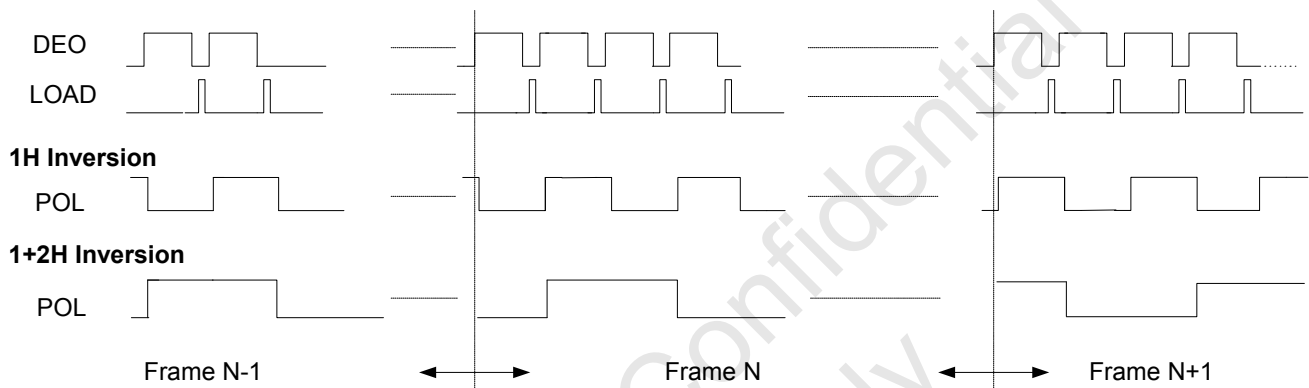


#### 5.4. CABC architecture

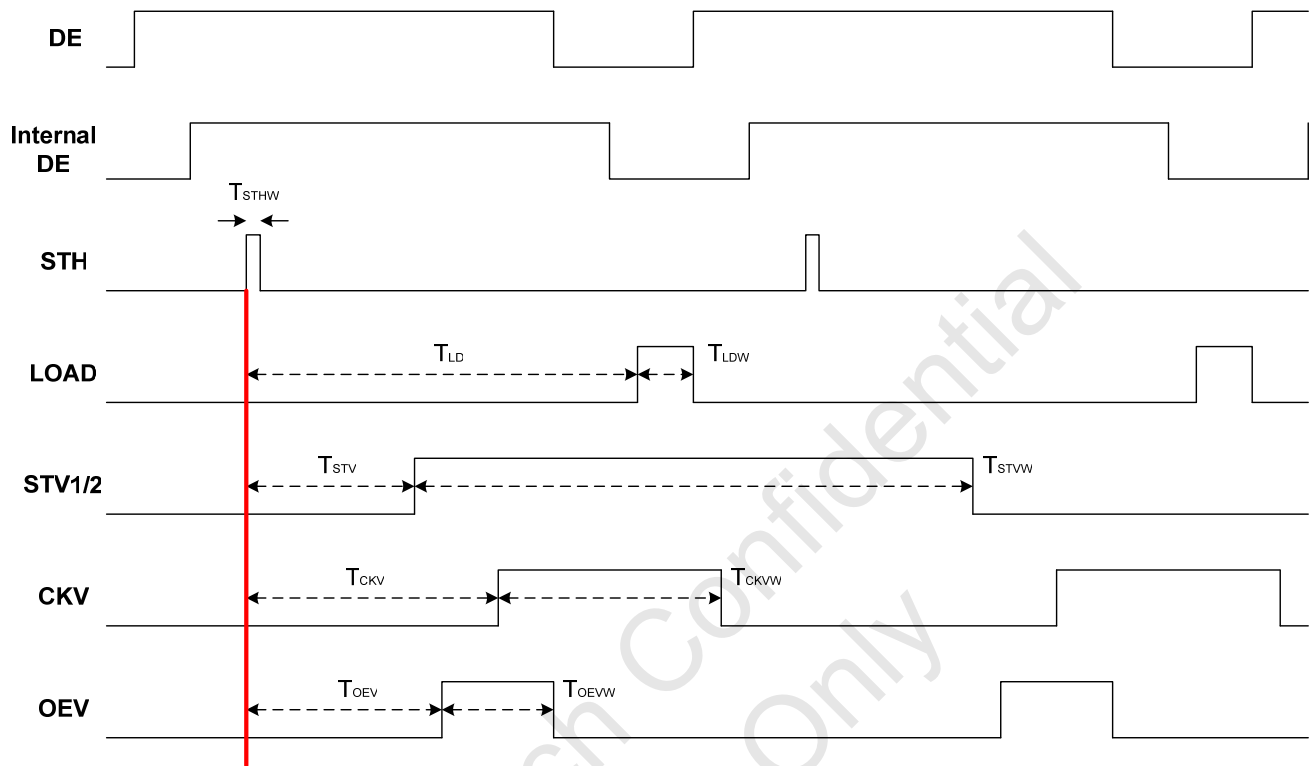
The OTA7001A support CABC function with external or internal programmable gamma buffer. The interface between gamma buffer and OTA7001A is I2C.

#### 5.5. TCON timing

##### 5.5.1. Polarity Control



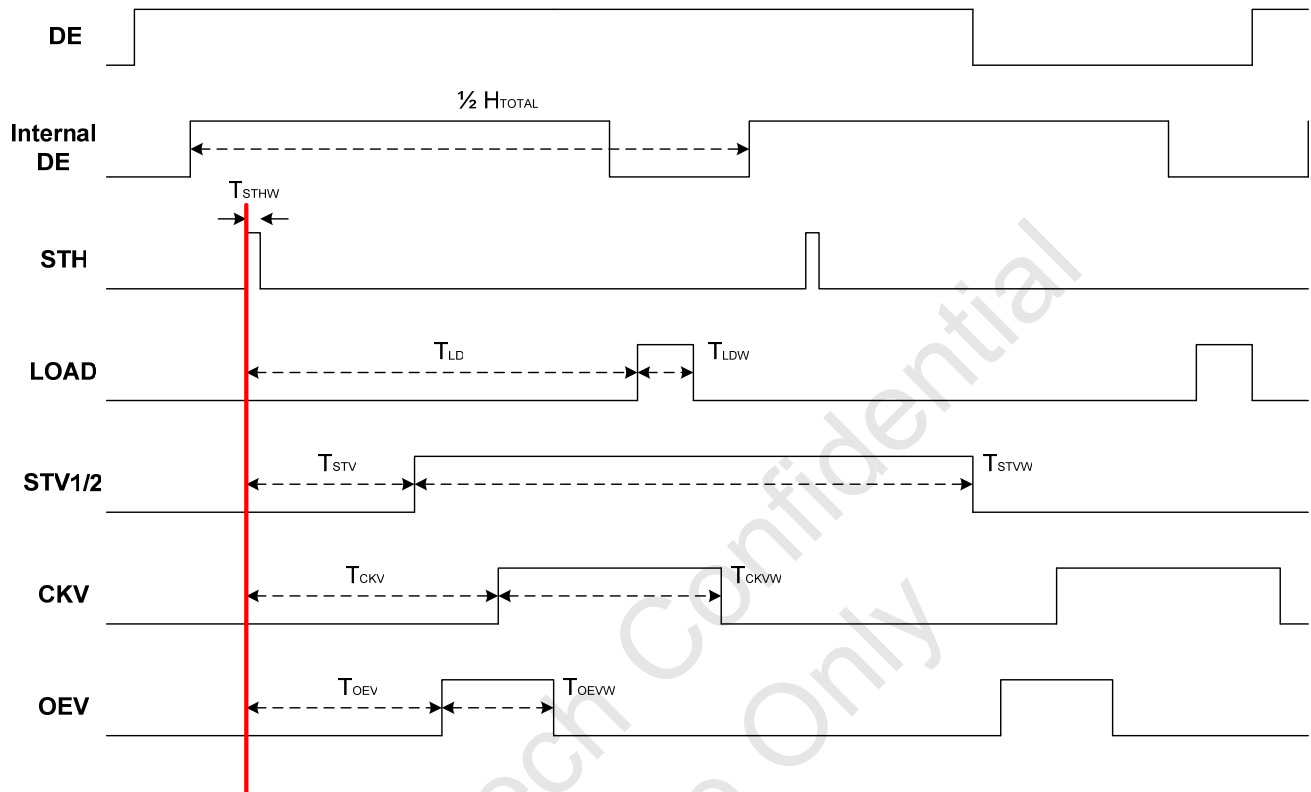
ORISE Tech Confidential  
For 橋電 Use Only

**5.5.2. Gate driver timing (Single Gate)**


	TLD	TLDW	TOEV	TOEVW	TCKV	TCKVW	TSTV	TSTVW	TSTHW	Unit
<b>Default</b>	TLD [9:0]	TLDW [9:0]	TOE1 [9:0]	TOE1W [9:0]	TCPV [9:0]	TCPVW [9:0]	TSTV [9:0]	TSTVW [9:0]	FIXED	Bit
<b>XGA</b>	TBD	TBD	TBD	TBD	TBD	TBD	TBD	1 LINE	1	$T_{CLK}$

Note: STH, LOAD and POL are internal signals inside OTA7001A



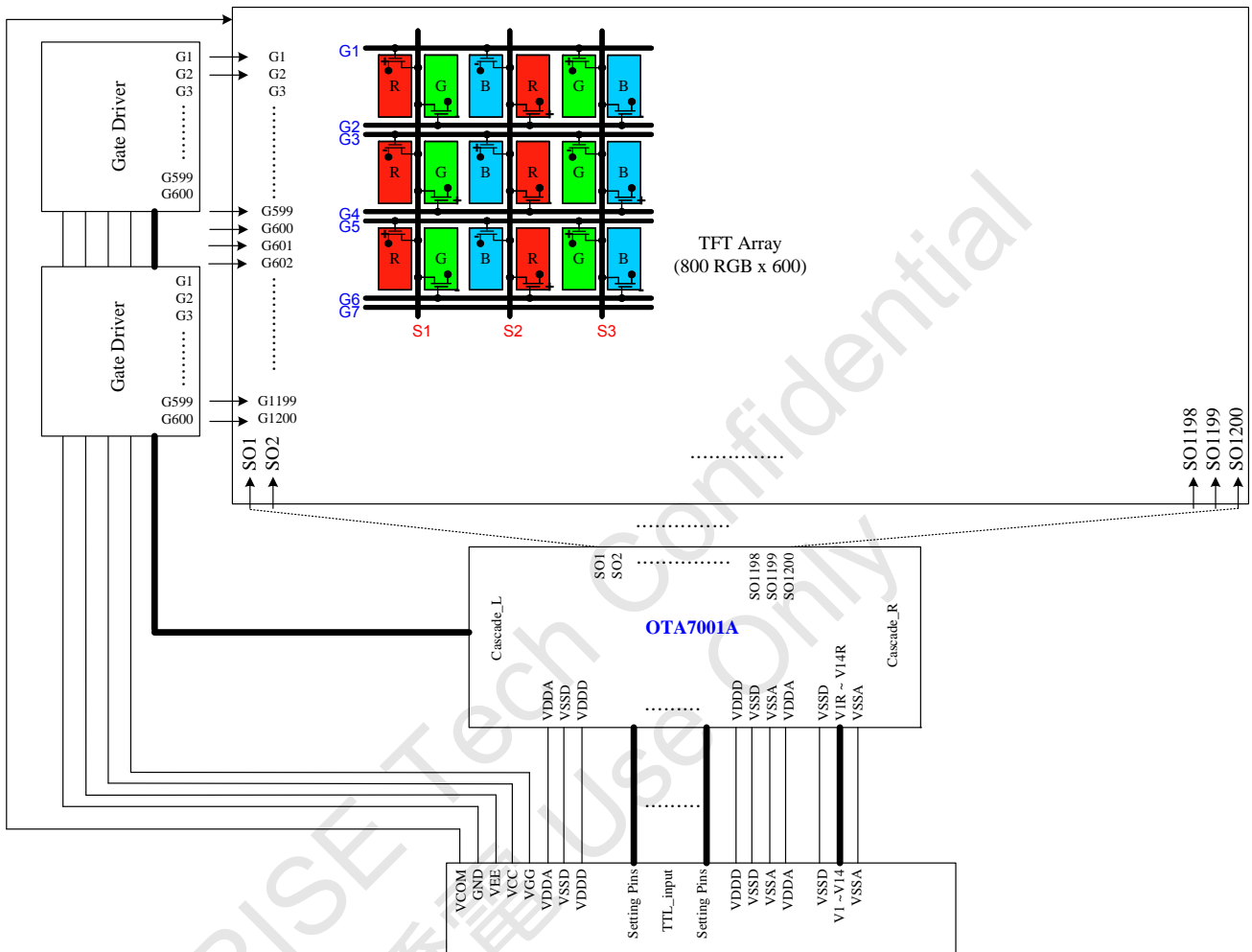
**5.5.3. Gate driver timing (Dual Gate)**


	TLD	TLDW	TOEV	TOEVW	TCKV	TCKVW	TSTV	TSTVW	TSTHW	Unit
<b>Default</b>	TLD [9:0]	TLDW [9:0]	TOE1 [9:0]	TOE1W [9:0]	TCPV [9:0]	TCPVW [9:0]	TSTV [9:0]	TSTVW [9:0]	FIXED	Bit
<b>XGA</b>	TBD	TBD	TBD	TBD	TBD	TBD	TBD	1 LINE	1	$T_{CLK}$

Note: STH, LOAD and POL are internal signals inside OTA7001A

## 5.6. Application Diagram

### 5.6.1. Dual Gate Application (DGEN=1, GATELOC=1), 800 x 600 as example

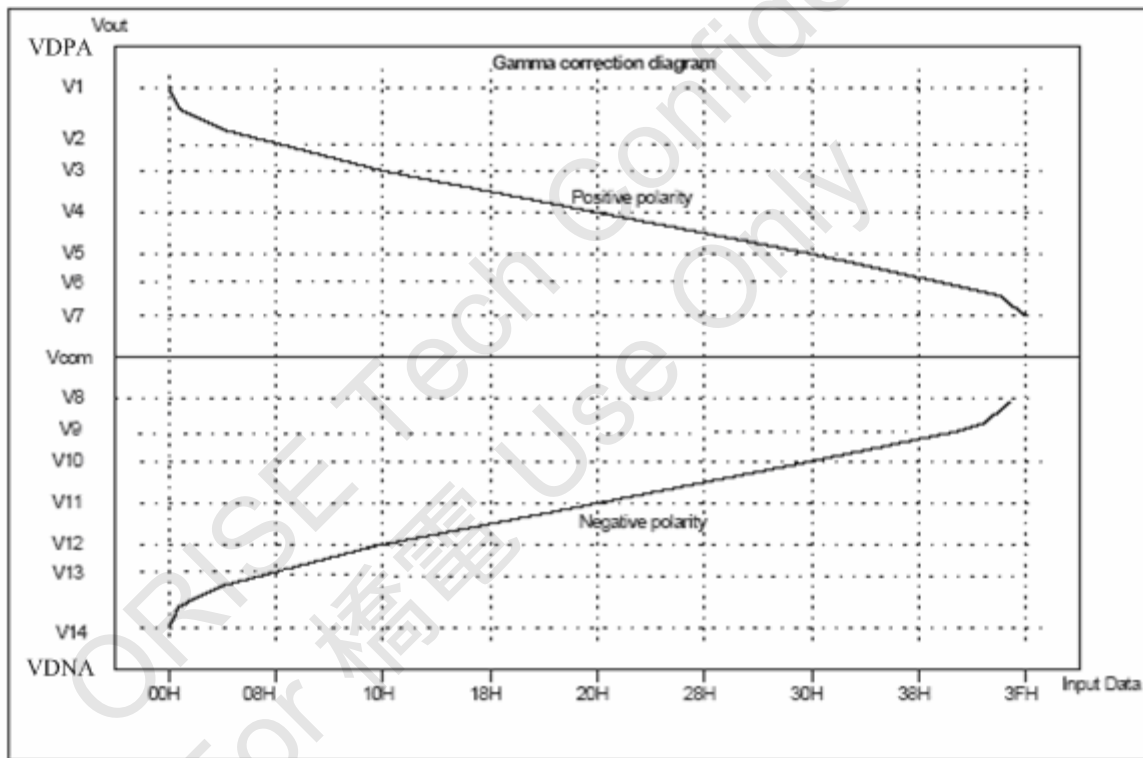


**5.7. Useful & useless channel of source driver**

DGSEN	RES[2:0]	Resolution	Useful channels #	Disable Channels
1	0xx	768x1024	1152ch	S577 ~ S624
	100	800x480	1200ch	
	101	800x600	1200ch	
	110	600x480	900ch	S451 ~ S750
	111	600x800	900ch	S451 ~ S750
0	xx0	1024x768(Mono-TFT)	1024ch	S517 ~ S684, S1193 ~ S1200
	xx1	960x1024(Mono-TFT)	960ch	S481 ~ S720

**5.8. Input Data VS Output Voltage**

The figure below shows the relationship between the input data and the output voltage. Refer to the following pages for the relative resistor values and voltage calculation method.



Remark:  $VDPA - 0.3 \geq V1 \geq V2 \geq V3 \geq V4 \geq V5 \geq V6 \geq V7$ ;  $V8 \geq V9 \geq V10 \geq V11 \geq V12 \geq V13 \geq V14 \geq VDPA + 0.3$

## 5.9. Gamma correction resistor ratio (GMA\_EN=1)

Name	Resistor
R0	10.48
R1	9.31
R2	8.15
R3	6.98
R4	5.82
R5	4.66
R6	3.88
R7	3.24
R8	2.81
R9	2.47
R10	2.21
R11	2.01
R12	1.81
R13	1.68
R14	1.59
R15	1.49
R16	1.38
R17	1.31
R18	1.24
R19	1.19
R20	1.14
R21	1.1
R22	1.05
R23	1.02
R24	0.99
R25	0.96
R26	0.93
R27	0.91
R28	0.89
R29	0.88
R30	0.86
R31	0.84

Name	Resistor
R32	0.83
R33	0.83
R34	0.82
R35	0.81
R36	0.81
R37	0.81
R38	0.81
R39	0.8
R40	0.8
R41	0.8
R42	0.81
R43	0.82
R44	0.83
R45	0.84
R46	0.85
R47	0.87
R48	0.88
R49	0.9
R50	0.93
R51	0.99
R52	1.02
R53	1.07
R54	1.17
R55	1.29
R56	1.41
R57	1.58
R58	1.79
R59	2.08
R60	2.59
R61	4.51
R62	35.72

**5.10. Output Voltage vs. Input Data (GMA\_EN=1)**

	Positive polarity Output Voltage	Negative polarity Output Voltage
<b>00H</b>	V1	V14
<b>01H</b>	$V1 + (V3-V1) \times 10.48 / 68.59$	$V14 + (V12-V14) \times 10.48 / 68.59$
<b>02H</b>	$V1 + (V3-V1) \times 19.79 / 68.59$	$V14 + (V12-V14) \times 19.79 / 68.59$
<b>03H</b>	$V1 + (V3-V1) \times 27.94 / 68.59$	$V14 + (V12-V14) \times 27.94 / 68.59$
<b>04H</b>	$V1 + (V3-V1) \times 34.92 / 68.59$	$V14 + (V12-V14) \times 34.92 / 68.59$
<b>05H</b>	$V1 + (V3-V1) \times 40.74 / 68.59$	$V14 + (V12-V14) \times 40.74 / 68.59$
<b>06H</b>	$V1 + (V3-V1) \times 45.4 / 68.59$	$V14 + (V12-V14) \times 45.4 / 68.59$
<b>07H</b>	$V1 + (V3-V1) \times 49.28 / 68.59$	$V14 + (V12-V14) \times 49.28 / 68.59$
<b>08H</b>	$V1 + (V3-V1) \times 52.52 / 68.59$	$V14 + (V12-V14) \times 52.52 / 68.59$
<b>09H</b>	$V1 + (V3-V1) \times 55.33 / 68.59$	$V14 + (V12-V14) \times 55.33 / 68.59$
<b>0AH</b>	$V1 + (V3-V1) \times 57.8 / 68.59$	$V14 + (V12-V14) \times 57.8 / 68.59$
<b>0BH</b>	$V1 + (V3-V1) \times 60.01 / 68.59$	$V14 + (V12-V14) \times 60.01 / 68.59$
<b>0CH</b>	$V1 + (V3-V1) \times 62.02 / 68.59$	$V14 + (V12-V14) \times 62.02 / 68.59$
<b>0DH</b>	$V1 + (V3-V1) \times 63.83 / 68.59$	$V14 + (V12-V14) \times 63.83 / 68.59$
<b>0EH</b>	$V1 + (V3-V1) \times 65.51 / 68.59$	$V14 + (V12-V14) \times 65.51 / 68.59$
<b>0FH</b>	$V1 + (V3-V1) \times 67.1 / 68.59$	$V14 + (V12-V14) \times 67.1 / 68.59$
<b>10H</b>	V3	V12
<b>11H</b>	$V3 + (V4-V3) \times 1.38 / 16.69$	$V12 + (V11-V12) \times 1.38 / 16.69$
<b>12H</b>	$V3 + (V4-V3) \times 2.69 / 16.69$	$V12 + (V11-V12) \times 2.69 / 16.69$
<b>13H</b>	$V3 + (V4-V3) \times 3.93 / 16.69$	$V12 + (V11-V12) \times 3.93 / 16.69$
<b>14H</b>	$V3 + (V4-V3) \times 5.12 / 16.69$	$V12 + (V11-V12) \times 5.12 / 16.69$
<b>15H</b>	$V3 + (V4-V3) \times 6.26 / 16.69$	$V12 + (V11-V12) \times 6.26 / 16.69$
<b>16H</b>	$V3 + (V4-V3) \times 7.36 / 16.69$	$V12 + (V11-V12) \times 7.36 / 16.69$
<b>17H</b>	$V3 + (V4-V3) \times 8.41 / 16.69$	$V12 + (V11-V12) \times 8.41 / 16.69$
<b>18H</b>	$V3 + (V4-V3) \times 9.43 / 16.69$	$V12 + (V11-V12) \times 9.43 / 16.69$
<b>19H</b>	$V3 + (V4-V3) \times 10.42 / 16.69$	$V12 + (V11-V12) \times 10.42 / 16.69$
<b>1AH</b>	$V3 + (V4-V3) \times 11.38 / 16.69$	$V12 + (V11-V12) \times 11.38 / 16.69$
<b>1BH</b>	$V3 + (V4-V3) \times 12.31 / 16.69$	$V12 + (V11-V12) \times 12.31 / 16.69$
<b>1CH</b>	$V3 + (V4-V3) \times 13.22 / 16.69$	$V12 + (V11-V12) \times 13.22 / 16.69$
<b>1DH</b>	$V3 + (V4-V3) \times 14.11 / 16.69$	$V12 + (V11-V12) \times 14.11 / 16.69$
<b>1EH</b>	$V3 + (V4-V3) \times 14.99 / 16.69$	$V12 + (V11-V12) \times 14.99 / 16.69$
<b>1FH</b>	$V3 + (V4-V3) \times 15.85 / 16.69$	$V12 + (V11-V12) \times 15.85 / 16.69$

	<b>Positive polarity Output Voltage</b>	<b>Negative polarity Output Voltage</b>
<b>20H</b>	V4	V11
<b>21H</b>	$V4 + (V5-V4) \times 0.83 / 13.14$	$V11 + (V10-V11) \times 0.83 / 13.14$
<b>22H</b>	$V4 + (V5-V4) \times 1.66 / 13.14$	$V11 + (V10-V11) \times 1.66 / 13.14$
<b>23H</b>	$V4 + (V5-V4) \times 2.48 / 13.14$	$V11 + (V10-V11) \times 2.48 / 13.14$
<b>24H</b>	$V4 + (V5-V4) \times 3.29 / 13.14$	$V11 + (V10-V11) \times 3.29 / 13.14$
<b>25H</b>	$V4 + (V5-V4) \times 4.1 / 13.14$	$V11 + (V10-V11) \times 4.1 / 13.14$
<b>26H</b>	$V4 + (V5-V4) \times 4.91 / 13.14$	$V11 + (V10-V11) \times 4.91 / 13.14$
<b>27H</b>	$V4 + (V5-V4) \times 5.72 / 13.14$	$V11 + (V10-V11) \times 5.72 / 13.14$
<b>28H</b>	$V4 + (V5-V4) \times 6.52 / 13.14$	$V11 + (V10-V11) \times 6.52 / 13.14$
<b>29H</b>	$V4 + (V5-V4) \times 7.32 / 13.14$	$V11 + (V10-V11) \times 7.32 / 13.14$
<b>2AH</b>	$V4 + (V5-V4) \times 8.12 / 13.14$	$V11 + (V10-V11) \times 8.12 / 13.14$
<b>2BH</b>	$V4 + (V5-V4) \times 8.93 / 13.14$	$V11 + (V10-V11) \times 8.93 / 13.14$
<b>2CH</b>	$V4 + (V5-V4) \times 9.75 / 13.14$	$V11 + (V10-V11) \times 9.75 / 13.14$
<b>2DH</b>	$V4 + (V5-V4) \times 10.58 / 13.14$	$V11 + (V10-V11) \times 10.58 / 13.14$
<b>2EH</b>	$V4 + (V5-V4) \times 11.42 / 13.14$	$V11 + (V10-V11) \times 11.42 / 13.14$
<b>2FH</b>	$V4 + (V5-V4) \times 12.27 / 13.14$	$V11 + (V10-V11) \times 12.27 / 13.14$
<b>30H</b>	V5	V10
<b>31H</b>	$V5 + (V7-V5) \times 0.88 / 57.93$	$V10 + (V8-V10) \times 0.88 / 57.93$
<b>32H</b>	$V5 + (V7-V5) \times 1.78 / 57.93$	$V10 + (V8-V10) \times 1.78 / 57.93$
<b>33H</b>	$V5 + (V7-V5) \times 2.71 / 57.93$	$V10 + (V8-V10) \times 2.71 / 57.93$
<b>34H</b>	$V5 + (V7-V5) \times 3.7 / 57.93$	$V10 + (V8-V10) \times 3.7 / 57.93$
<b>35H</b>	$V5 + (V7-V5) \times 4.72 / 57.93$	$V10 + (V8-V10) \times 4.72 / 57.93$
<b>36H</b>	$V5 + (V7-V5) \times 5.79 / 57.93$	$V10 + (V8-V10) \times 5.79 / 57.93$
<b>37H</b>	$V5 + (V7-V5) \times 6.96 / 57.93$	$V10 + (V8-V10) \times 6.96 / 57.93$
<b>38H</b>	$V5 + (V7-V5) \times 8.25 / 57.93$	$V10 + (V8-V10) \times 8.25 / 57.93$
<b>39H</b>	$V5 + (V7-V5) \times 9.66 / 57.93$	$V10 + (V8-V10) \times 9.66 / 57.93$
<b>3AH</b>	$V5 + (V7-V5) \times 11.24 / 57.93$	$V10 + (V8-V10) \times 11.24 / 57.93$
<b>3BH</b>	$V5 + (V7-V5) \times 13.03 / 57.93$	$V10 + (V8-V10) \times 13.03 / 57.93$
<b>3CH</b>	$V5 + (V7-V5) \times 15.11 / 57.93$	$V10 + (V8-V10) \times 15.11 / 57.93$
<b>3DH</b>	$V5 + (V7-V5) \times 17.7 / 57.93$	$V10 + (V8-V10) \times 17.7 / 57.93$
<b>3EH</b>	$V5 + (V7-V5) \times 22.21 / 57.93$	$V10 + (V8-V10) \times 22.21 / 57.93$
<b>3FH</b>	V7	V8

## 6. ELECTRICAL SPECIFICATIONS

### 6.1. Absolute Maximum Ratings

#### VOLTAGE

(GND = AGND = 0V, TA=25°C)

Parameter	Symbol	MIN	MAX	UNIT
Digital Supply Voltage	VDDD	-0.5	+5.0v	V
Analog Supply Voltage, V1~V14	VDDA	-0.5	+15.0v	V

Note1 : The maximum applicable voltage on any pin is respected to 0V.

Note2 : Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.

#### TEMPERATURE

Parameter	MIN	MAX	UNIT
Operating temperature	-20	85	°C
Storage temperature	-55	125	°C

#### \*Comments

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device.

These are stress ratings only. Functional operation of this device at these or any other conditions above those

Indicated in the operational sections of this specification is not implied and exposed to absolute maximum rating

Conditions for extended periods may affect reliability.

### 6.2. Recommended Operating Conditions

(VSSD = VSSA = 0V, TA= -20 to +85°C)

Parameter	Symbol	MIN.	Typ.	MAX.	UNIT
Digital supply voltage	VDDD	2.3	-	3.6	V
Analog supply voltage	VDDA	6.5	-	13.5	V
Digital input voltage	VIN	0	-	VDDD	V

**6.3. DC Characteristics**
**6.3.1. System Characteristics**

(VDDD=2.3 to 3.6V, VDDA=6.5 to 13.5V, VSSD=VSSA=0V, TA=-20 to +85°C)

Parameter	Symbol	MIN.	Typ.	MAX.	UNIT	Conditions
Low level input voltage	Vil	0	-	0.3*VDDD	V	For the digital circuit
High level input voltage	Vih	0.7*VDDD	-	VDDD	V	For the digital circuit
Input leakage current	Ii	-	-	+1	μA	For the digital circuit
High level output voltage	Voh	VDDD-0.4	-	-	V	Ioh=-400μA
Low level output voltage	Vol	-	-	VSSD+0.4	V	Iol=+400μA
Pull low/high resistor	Ri	50K	100K	200K	ohm	For the digital input pin @ VDDD=3.3V
Digital Operation current	Idd	-	TBD	TBD	mA	Fclk=45 MHz, FLD=48KHz, VDDD=3.3V
Digital Stand-by current	Ist1		TBD	TBD	μA	Clock & all functions are stopped.
Analog Operation current	Idda		TBD	TBD	mA	No load, Fclk=45MHz, FLD=42KHz @ VDDA=10V, V1=8V, V14=0.4V
Analog Stand-by current	Ist2		TBD	TBD	μA	No load, Clock & all functions are stopped
Input level of V1 ~ V7	Vref1	0.4*VDDA	-	VDDA-1	V	Gamma correction voltage input
Input level of V8 ~ V14	Vref2	0.1	-	0.6*VDDA	V	Gamma correction voltage input
Output Voltage deviation	Vod1	-	±20	±35	mV	Vo = VSSA+0.1V ~ VSSA +0.5V & Vo = VSSA -0.5V ~ VSSA -0.1V
Output Voltage deviation	Vod2	-	±15	±20	mV	Vo = VSSA +0.5V ~ VSSA -0.5V
Output Voltage offset Between Chips	Voc	-	-	±20	mV	Vo = VSSA +0.5V ~ VSSA -0.5V
Dynamic Range of Output	Vdr	0.1	-	VDDA-0.1	V	SO1 ~ SO12000
Sinking Current of Outputs	IOLy	80	-	-	μA	SO1 ~ SO12000; Vo=0.1V v.s 1.0V VDDA=13.5V
Driving Current of Outputs	IOHy	80	-	-	μA	SO1 ~ SO12000; Vo=13.4V v.s 12.5V VDDA=13.5V
Resistance of Gamma Table	Rg	0.7*Rn	1.0*Rn	1.3*Rn	ohm	Rn : Internal gamma resistor



**6.4. AC Characteristics**
**6.4.1. System AC Characteristics**

(VDDD=2.3 to 3.6V,AVDD=6.5 to 13.5V, GND=AGND=0V,TA=-20 to +85°C)

Parameter	Symbol	MIN.	Typ.	MAX.	UNIT	Conditions
VDD Power On Slew rate	T	-	-	20	ms	From 0V to 90% VDD
RSTB pulse width	T	10	-	-	us	CLKIN = 45MHz
CLKIN cycle time	Tcph	20	-	-	ns	
CLKIN pulse duty	Tcwh	40	50	60	%	
VSD setup time	Tvst	8	-	-	ns	
VSD hold time	Tvhd	8	-	-	ns	
HSD setup time	Thst	8	-	-	ns	
HSD hold time	Thhd	8	-	-	ns	
Data set-up time	Tdsu	8	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
Data hold time	Tdhd	8	-	-	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
DE set-up time	Tesu	8	-	-	ns	
DE hold time	Tehd	8	-	-	ns	
Output stable time	Tsst	-	-	6	us	10% to 90% target voltage. CL=120pF, R=10K ohm

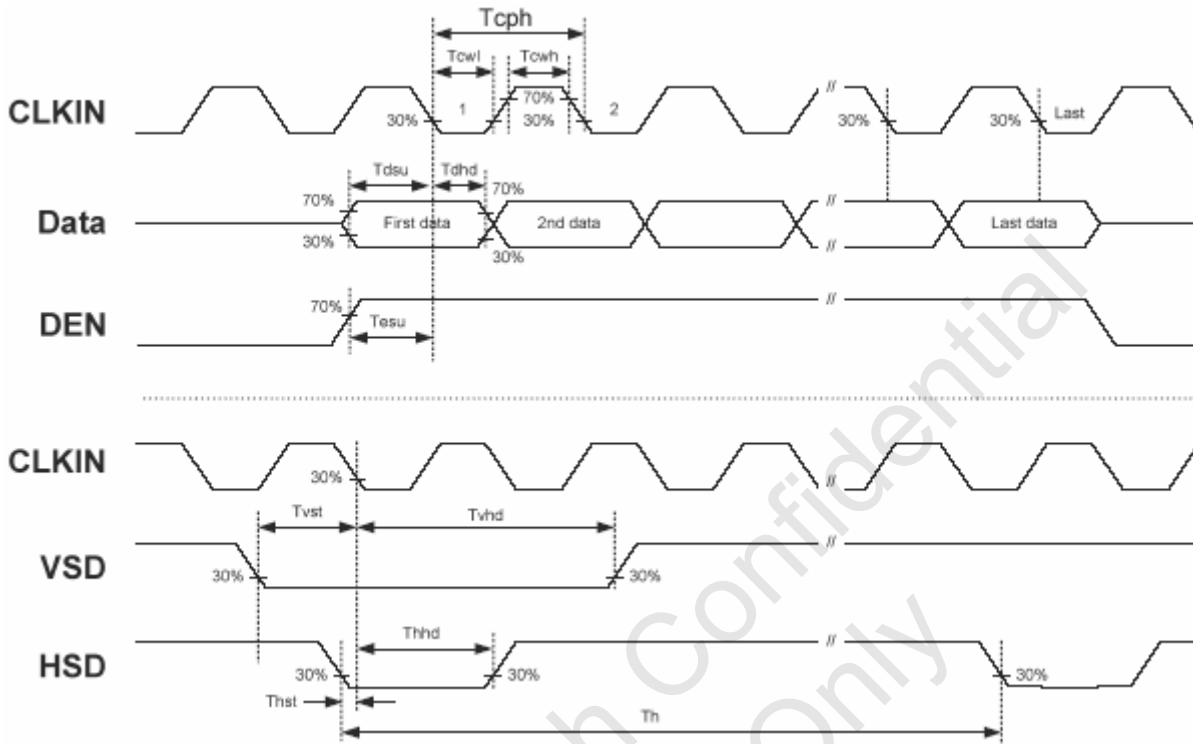
**6.4.2. Parallel RGB AC Characteristics**

(VDDD=2.3 to 3.6V,AVDD=6.5 to 13.5V, GND=AGND=0V,TA=-20 to +85°C)

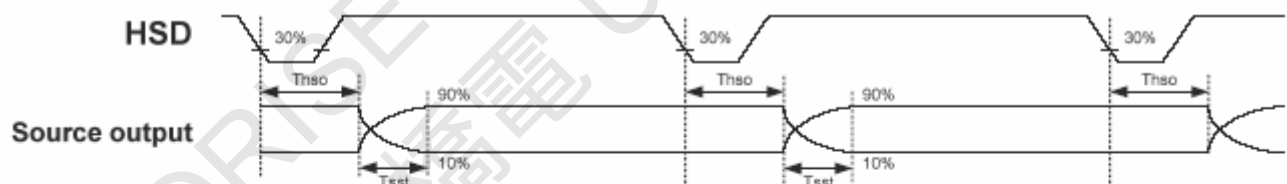
Parameter	Symbol	MIN.	Typ.	MAX.	UNIT	Conditions
CLKIN Frequency	Fclk	-	33	50	MHz	VDDD = 2.3V ~ 3.6V
CLKIN Cycle Time	Tclk	20	30	-	ns	
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso	-	Tld		CLKIN	
Time from HSD to LD	Thld	-	Tld		CLKIN	
Time from HSD to STV	Thstv	-	2		CLKIN	
Time from HSD to CKV	Thckv	-	20		CLKIN	
Time from HSD to OEV	Thoev	-	4		CLKIN	
LD Pulse	Twld	-	10		CLKIN	
CKV Pulse Width	Twckv	-	66		CLKIN	
OEV Pulse Width	Twoev	-	Tld+10		CLKIN	

Timing Diagram

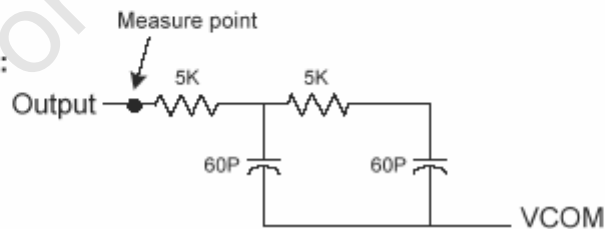
Input Clock and Data Timing Diagram



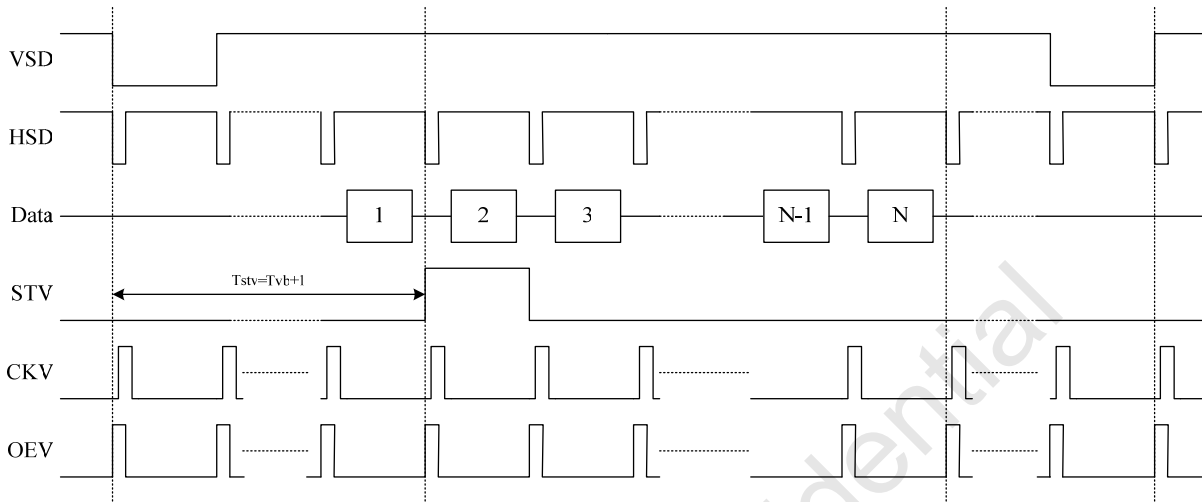
Source Output Timing Diagram (Single Gate)



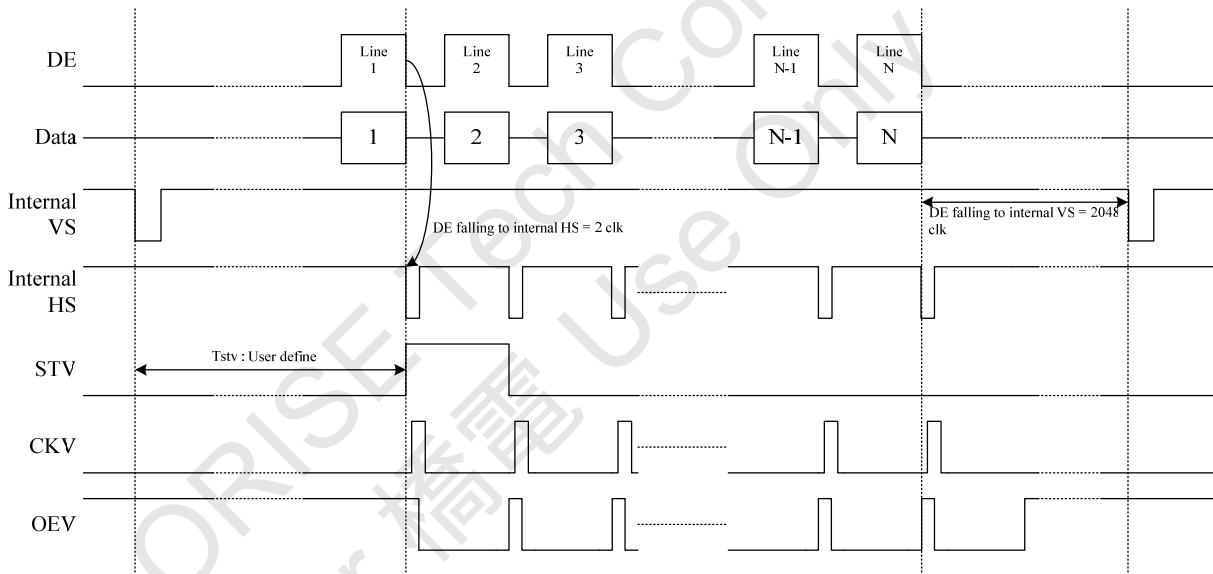
Output load condition :



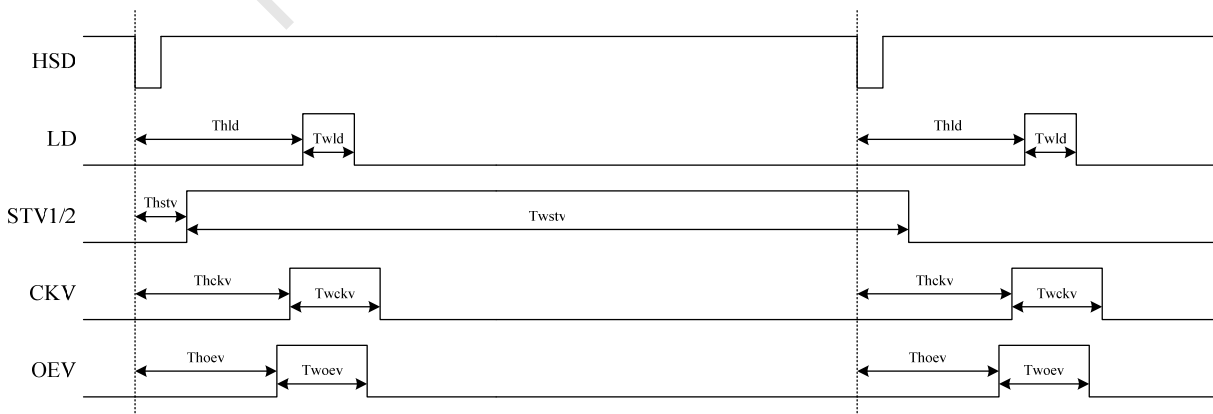
Vertical Timing Diagram HV (Single Gate)



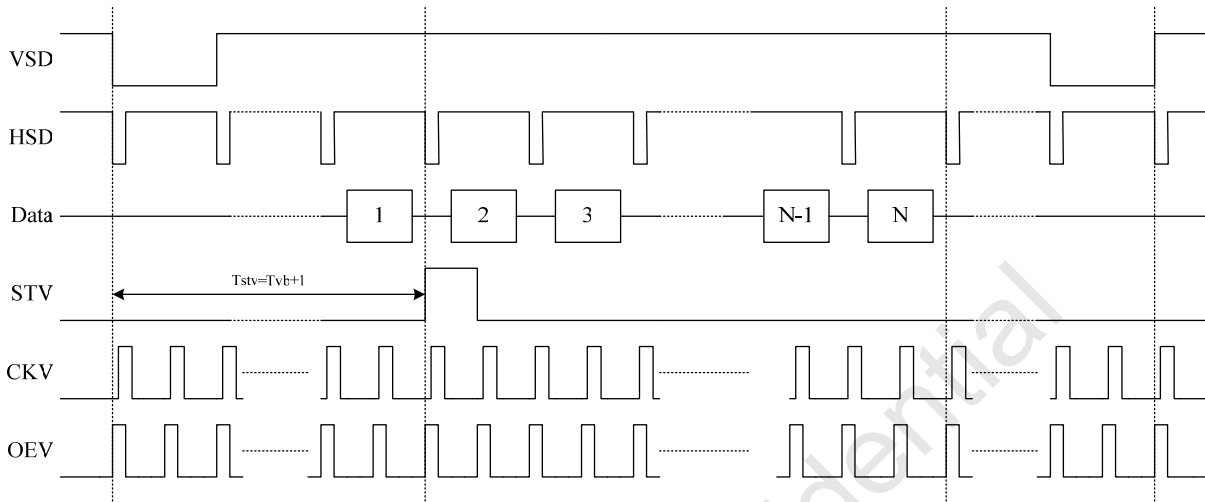
Vertical Timing Diagram DE (Single Gate)



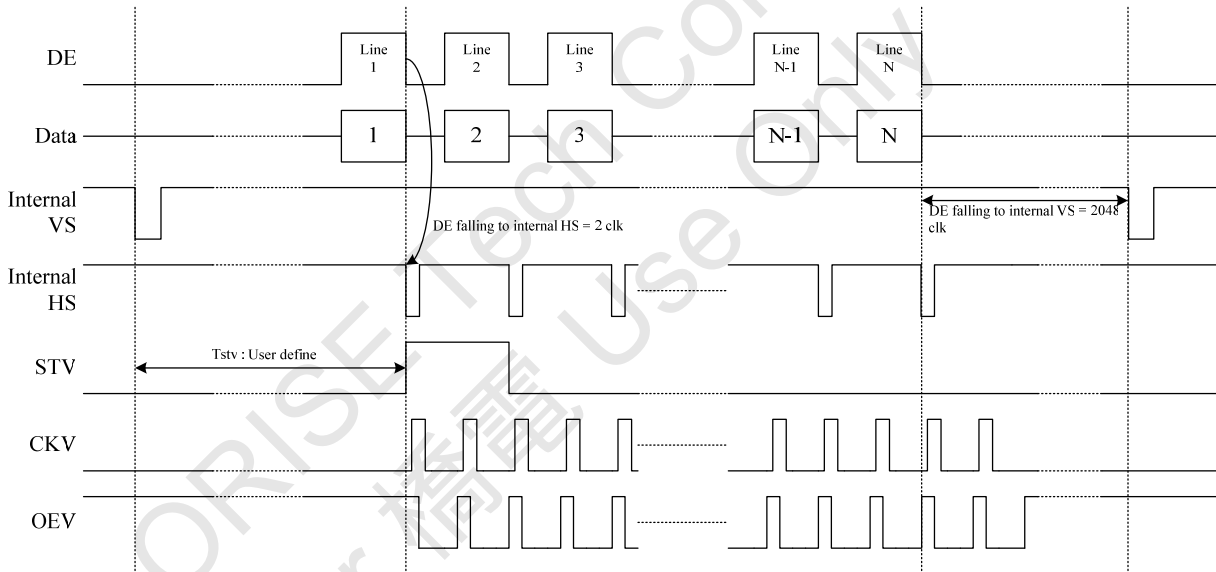
Gate output timing diagram (Single Gate)



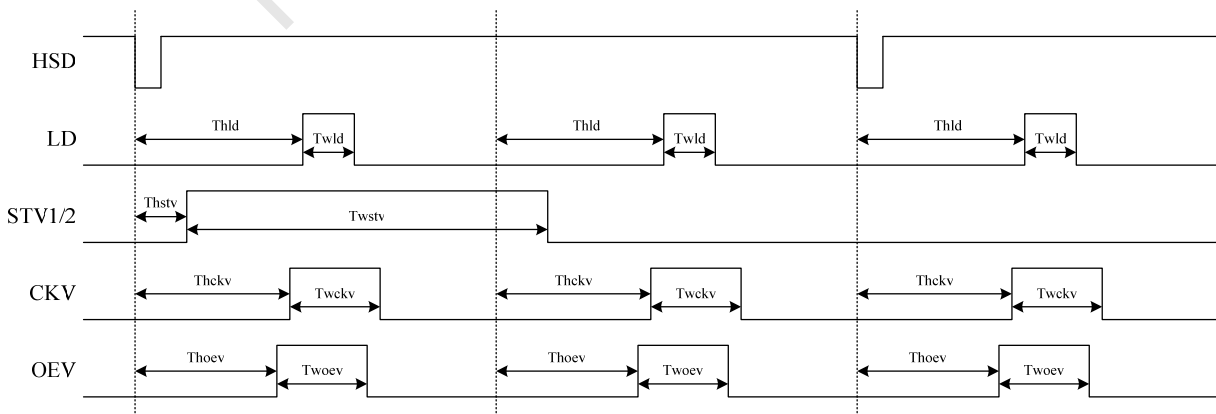
Vertical Timing Diagram HV (Dual Gate)



Vertical Timing Diagram DE (Dual Gate)

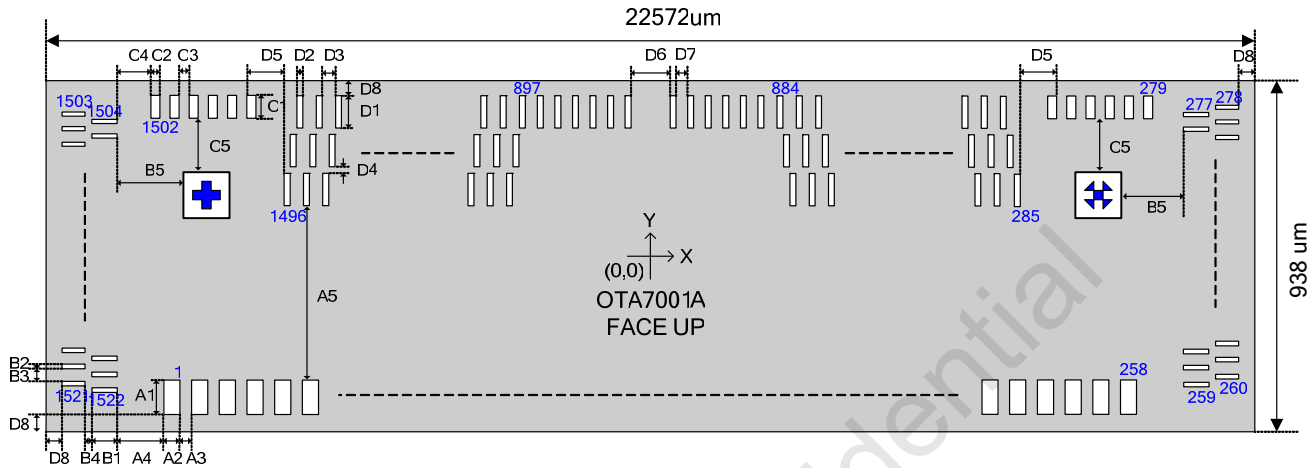


Gate output timing diagram (Dual Gate)



## 7. CHIP INFORMATION

### 7.1. PAD Assignment



**Note1:** Chip size included scribe line.

Symbol	Size	Symbol	Size	Symbol	Size	Symbol	Size	Symbol	Size
A1	110	B1	100	C1	70	D1	110	D6	450
A2	65	B2	30	C2	30	D2	17	D7	17
A3	20	B3	50	C3	20	D3	34	D8	57
A4	44	B4	30	C4	70	D4	30	Unit : um	
A5	324	B5	168.5	C5	191.5	D5	50		

### 7.2. PAD Dimension

Item	PAD No.	Size		Unit
		X	Y	
Chip Size	-	22572	938	μm
Chip thickness	-	280 ± 10		
Pad pitch	1-258	85	-	
	1503-1522, 259-278	130	40	
	279-284, 1497-1502	50	-	
	285-884, 897-1496	17	140	
Pad size	885-890, 891-896	34	-	
	1-258	65	110	
	1503-1522, 259-278	100	30	
	279-284, 1497-1502	30	70	
	285-1496	17	110	

**Note1:** Chip size included scribe line.

**Note2:** The Chip thickness is an average value. Please refer to ISO document to get the specific Chip thickness information.

**7.3. Pad Location**

PAD NO.	PAD NAME	X	Y
1	DUM_GND	-10922.5	-357
2	DUM_GND	-10837.5	-357
3	TEST11	-10752.5	-357
4	COM_PASSA	-10667.5	-357
5	COM_PASSA	-10582.5	-357
6	VSSA	-10497.5	-357
7	VSSA	-10412.5	-357
8	VSSA	-10327.5	-357
9	VSSA	-10242.5	-357
10	TEST0	-10157.5	-357
11	DUM_GND	-10072.5	-357
12	V1R	-9987.5	-357
13	V1R	-9902.5	-357
14	DUM_GND	-9817.5	-357
15	V2R	-9732.5	-357
16	V2R	-9647.5	-357
17	DUM_GND	-9562.5	-357
18	V3R	-9477.5	-357
19	V3R	-9392.5	-357
20	DUM_GND	-9307.5	-357
21	V4R	-9222.5	-357
22	V4R	-9137.5	-357
23	DUM_GND	-9052.5	-357
24	V5R	-8967.5	-357
25	V5R	-8882.5	-357
26	DUM_GND	-8797.5	-357
27	V6R	-8712.5	-357
28	V6R	-8627.5	-357
29	DUM_GND	-8542.5	-357
30	V7R	-8457.5	-357
31	V7R	-8372.5	-357
32	DUM_GND	-8287.5	-357
33	V8R	-8202.5	-357
34	V8R	-8117.5	-357
35	DUM_GND	-8032.5	-357
36	V9R	-7947.5	-357
37	V9R	-7862.5	-357
38	DUM_GND	-7777.5	-357
39	V10R	-7692.5	-357
40	V10R	-7607.5	-357
41	DUM_GND	-7522.5	-357
42	V11R	-7437.5	-357
43	V11R	-7352.5	-357
44	DUM_GND	-7267.5	-357
45	V12R	-7182.5	-357
46	V12R	-7097.5	-357
47	DUM_GND	-7012.5	-357
48	V13R	-6927.5	-357
49	V13R	-6842.5	-357
50	DUM_GND	-6757.5	-357
51	V14R	-6672.5	-357
52	V14R	-6587.5	-357
53	DUM_GND	-6502.5	-357
54	TEST1	-6417.5	-357
55	TEST2	-6332.5	-357

PAD NO.	PAD NAME	X	Y
56	TEST3	-6247.5	-357
57	TEST4	-6162.5	-357
58	TEST5	-6077.5	-357
59	REV	-5992.5	-357
60	BIST	-5907.5	-357
61	BIST	-5822.5	-357
62	PWM_IN	-5737.5	-357
63	VDDA	-5652.5	-357
64	VDDA	-5567.5	-357
65	VDDA	-5482.5	-357
66	VDDA	-5397.5	-357
67	VSSA	-5312.5	-357
68	VSSA	-5227.5	-357
69	VSSA	-5142.5	-357
70	VSSA	-5057.5	-357
71	VSSA	-4972.5	-357
72	VSSD	-4887.5	-357
73	VSSD	-4802.5	-357
74	VSSD	-4717.5	-357
75	VSSD	-4632.5	-357
76	VSSD	-4547.5	-357
77	VSSD	-4462.5	-357
78	BLKEN	-4377.5	-357
79	BLKEN	-4292.5	-357
80	DUM_GND	-4207.5	-357
81	VDDD	-4122.5	-357
82	VDDD	-4037.5	-357
83	VDDD	-3952.5	-357
84	VDDD	-3867.5	-357
85	TEST6	-3782.5	-357
86	DGEN	-3697.5	-357
87	DGEN	-3612.5	-357
88	OTP_VDD	-3527.5	-357
89	RES2	-3442.5	-357
90	RES2	-3357.5	-357
91	SCL	-3272.5	-357
92	GATELOC	-3187.5	-357
93	GATELOC	-3102.5	-357
94	SDA	-3017.5	-357
95	RES0	-2932.5	-357
96	RES0	-2847.5	-357
97	CABC_PWM	-2762.5	-357
98	CABC_EN	-2677.5	-357
99	RES1	-2592.5	-357
100	RES1	-2507.5	-357
101	OTP_WEN	-2422.5	-357
102	OTP_WEN	-2337.5	-357
103	DUM_GND	-2252.5	-357
104	VSYN	-2167.5	-357
105	VSYN	-2082.5	-357
106	DUM_GND	-1997.5	-357
107	HSYN	-1912.5	-357
108	HSYN	-1827.5	-357
109	DUM_GND	-1742.5	-357
110	DE	-1657.5	-357

PAD NO.	PAD NAME	X	Y
111	DE	-1572.5	-357
112	DUM_GND	-1487.5	-357
113	DCLK	-1402.5	-357
114	DCLK	-1317.5	-357
115	DUM_GND	-1232.5	-357
116	DB7	-1147.5	-357
117	DB7	-1062.5	-357
118	DB6	-977.5	-357
119	DB6	-892.5	-357
120	DUM_GND	-807.5	-357
121	DB5	-722.5	-357
122	DB5	-637.5	-357
123	DB4	-552.5	-357
124	DB4	-467.5	-357
125	DUM_GND	-382.5	-357
126	DB3	-297.5	-357
127	DB3	-212.5	-357
128	DB2	-127.5	-357
129	DB2	-42.5	-357
130	DUM_GND	42.5	-357
131	DB1	127.5	-357
132	DB1	212.5	-357
133	DB0	297.5	-357
134	DB0	382.5	-357
135	DUM_GND	467.5	-357
136	DG7	552.5	-357
137	DG7	637.5	-357
138	DG6	722.5	-357
139	DG6	807.5	-357
140	DUM_GND	892.5	-357
141	DG5	977.5	-357
142	DG5	1062.5	-357
143	DG4	1147.5	-357
144	DG4	1232.5	-357
145	DUM_GND	1317.5	-357
146	DG3	1402.5	-357
147	DG3	1487.5	-357
148	DG2	1572.5	-357
149	DG2	1657.5	-357
150	DUM_GND	1742.5	-357
151	DG1	1827.5	-357
152	DG1	1912.5	-357
153	DG0	1997.5	-357
154	DG0	2082.5	-357
155	DUM_GND	2167.5	-357
156	DR7	2252.5	-357
157	DR7	2337.5	-357
158	D06	2422.5	-357
159	D06	2507.5	-357
160	DUM_GND	2592.5	-357
161	DR5	2677.5	-357
162	DR5	2762.5	-357
163	DR4	2847.5	-357
164	DR4	2932.5	-357
165	DUM_GND	3017.5	-357

PAD NO.	PAD NAME	X	Y
166	DR3	3102.5	-357
167	DR3	3187.5	-357
168	DR2	3272.5	-357
169	DR2	3357.5	-357
170	DUM_GND	3442.5	-357
171	DR1	3527.5	-357
172	DR1	3612.5	-357
173	DR0	3697.5	-357
174	DR0	3782.5	-357
175	DUM_GND	3867.5	-357
176	TEST7	3952.5	-357
177	MODE	4037.5	-357
178	MODE	4122.5	-357
179	CLKPOL	4207.5	-357
180	CLKPOL	4292.5	-357
181	DUM_GND	4377.5	-357
182	DITHB	4462.5	-357
183	DITHB	4547.5	-357
184	VSET	4632.5	-357
185	SHLR	4717.5	-357
186	SHLR	4802.5	-357
187	DUM_GND	4887.5	-357
188	UPDN	4972.5	-357
189	UPDN	5057.5	-357
190	DUM_GND	5142.5	-357
191	STBYB	5227.5	-357
192	STBYB	5312.5	-357
193	DUM_GND	5397.5	-357
194	RSTB	5482.5	-357
195	RSTB	5567.5	-357
196	DUM_GND	5652.5	-357
197	VDDD	5737.5	-357
198	VDDD	5822.5	-357
199	VDDD	5907.5	-357
200	VDDD	5992.5	-357
201	GMA_EN	6077.5	-357
202	VSSD	6162.5	-357
203	VSSD	6247.5	-357
204	VSSD	6332.5	-357
205	VSSD	6417.5	-357
206	TEST8	6502.5	-357
207	V14L	6587.5	-357
208	V14L	6672.5	-357
209	DUM_GND	6757.5	-357
210	V13L	6842.5	-357
211	V13L	6927.5	-357
212	DUM_GND	7012.5	-357
213	V12L	7097.5	-357
214	V12L	7182.5	-357
215	DUM_GND	7267.5	-357
216	V11L	7352.5	-357
217	V11L	7437.5	-357
218	DUM_GND	7522.5	-357
219	V10L	7607.5	-357
220	V10L	7692.5	-357
221	DUM_GND	7777.5	-357
222	V9L	7862.5	-357

PAD NO.	PAD NAME	X	Y
223	V9L	7947.5	-357
224	DUM_GND	8032.5	-357
225	V8L	8117.5	-357
226	V8L	8202.5	-357
227	DUM_GND	8287.5	-357
228	V7L	8372.5	-357
229	V7L	8457.5	-357
230	DUM_GND	8542.5	-357
231	V6L	8627.5	-357
232	V6L	8712.5	-357
233	DUM_GND	8797.5	-357
234	V5L	8882.5	-357
235	V5L	8967.5	-357
236	DUM_GND	9052.5	-357
237	V4L	9137.5	-357
238	V4L	9222.5	-357
239	DUM_GND	9307.5	-357
240	V3L	9392.5	-357
241	V3L	9477.5	-357
242	DUM_GND	9562.5	-357
243	V2L	9647.5	-357
244	V2L	9732.5	-357
245	DUM_GND	9817.5	-357
246	V1L	9902.5	-357
247	V1L	9987.5	-357
248	DUM_GND	10072.5	-357
249	VDDA	10157.5	-357
250	VDDA	10242.5	-357
251	VDDA	10327.5	-357
252	VDDA	10412.5	-357
253	TEST9	10497.5	-357
254	COM_PASSB	10582.5	-357
255	COM_PASSB	10667.5	-357
256	TEST10	10752.5	-357
257	DUM_GND	10837.5	-357
258	DUM_GND	10922.5	-357
259	DUM_GND	11049	-363
260	DUM_GND	11179	-323
261	DUM_GND	11049	-283
262	STBN_L	11179	-243
263	DUM_GND	11049	-203
264	DUM_GND	11179	-163
265	DUM_GND	11049	-123
266	STV1_L	11179	-83
267	DUM_GND	11049	-43
268	STV2_L	11179	-3
269	DUM_GND	11049	37
270	STV1_L	11179	77
271	DUM_GND	11049	117
272	CKV_L	11179	157
273	DUM_GND	11049	197
274	UD_L	11179	237
275	DUM_GND	11049	277
276	OEVL	11179	317
277	DUM_GND	11049	357
278	HSD_L	11179	397
279	XON_L	10914	377

PAD NO.	PAD NAME	X	Y
280	XON_L	10864	377
281	DUM_GND	10814	377
282	COM_PASSB	10764	377
283	COM_PASSB	10714	377
284	DUM_GND	10664	377
285	SO1	10620.5	77
286	SO2	10603.5	217
287	SO3	10586.5	357
288	SO4	10569.5	77
289	SO5	10552.5	217
290	SO6	10535.5	357
291	SO7	10518.5	77
292	SO8	10501.5	217
293	SO9	10484.5	357
294	SO10	10467.5	77
295	SO11	10450.5	217
296	SO12	10433.5	357
297	SO13	10416.5	77
298	SO14	10399.5	217
299	SO15	10382.5	357
300	SO16	10365.5	77
301	SO17	10348.5	217
302	SO18	10331.5	357
303	SO19	10314.5	77
304	SO20	10297.5	217
305	SO21	10280.5	357
306	SO22	10263.5	77
307	SO23	10246.5	217
308	SO24	10229.5	357
309	SO25	10212.5	77
310	SO26	10195.5	217
311	SO27	10178.5	357
312	SO28	10161.5	77
313	SO29	10144.5	217
314	SO30	10127.5	357
315	SO31	10110.5	77
316	SO32	10093.5	217
317	SO33	10076.5	357
318	SO34	10059.5	77
319	SO35	10042.5	217
320	SO36	10025.5	357
321	SO37	10008.5	77
322	SO38	9991.5	217
323	SO39	9974.5	357
324	SO40	9957.5	77
325	SO41	9940.5	217
326	SO42	9923.5	357
327	SO43	9906.5	77
328	SO44	9889.5	217
329	SO45	9872.5	357
330	SO46	9855.5	77
331	SO47	9838.5	217
332	SO48	9821.5	357
333	SO49	9804.5	77
334	SO50	9787.5	217
335	SO51	9770.5	357
336	SO52	9753.5	77

PAD NO.	PAD NAME	X	Y
337	SO53	9736.5	217
338	SO54	9719.5	357
339	SO55	9702.5	77
340	SO56	9685.5	217
341	SO57	9668.5	357
342	SO58	9651.5	77
343	SO59	9634.5	217
344	SO60	9617.5	357
345	SO61	9600.5	77
346	SO62	9583.5	217
347	SO63	9566.5	357
348	SO64	9549.5	77
349	SO65	9532.5	217
350	SO66	9515.5	357
351	SO67	9498.5	77
352	SO68	9481.5	217
353	SO69	9464.5	357
354	SO70	9447.5	77
355	SO71	9430.5	217
356	SO72	9413.5	357
357	SO73	9396.5	77
358	SO74	9379.5	217
359	SO75	9362.5	357
360	SO76	9345.5	77
361	SO77	9328.5	217
362	SO78	9311.5	357
363	SO79	9294.5	77
364	SO80	9277.5	217
365	SO81	9260.5	357
366	SO82	9243.5	77
367	SO83	9226.5	217
368	SO84	9209.5	357
369	SO85	9192.5	77
370	SO86	9175.5	217
371	SO87	9158.5	357
372	SO88	9141.5	77
373	SO89	9124.5	217
374	SO90	9107.5	357
375	SO91	9090.5	77
376	SO92	9073.5	217
377	SO93	9056.5	357
378	SO94	9039.5	77
379	SO95	9022.5	217
380	SO96	9005.5	357
381	SO97	8988.5	77
382	SO98	8971.5	217
383	SO99	8954.5	357
384	SO100	8937.5	77
385	SO101	8920.5	217
386	SO102	8903.5	357
387	SO103	8886.5	77
388	SO104	8869.5	217
389	SO105	8852.5	357
390	SO106	8835.5	77
391	SO107	8818.5	217
392	SO108	8801.5	357
393	SO109	8784.5	77

PAD NO.	PAD NAME	X	Y
394	SO110	8767.5	217
395	SO111	8750.5	357
396	SO112	8733.5	77
397	SO113	8716.5	217
398	SO114	8699.5	357
399	SO115	8682.5	77
400	SO116	8665.5	217
401	SO117	8648.5	357
402	SO118	8631.5	77
403	SO119	8614.5	217
404	SO120	8597.5	357
405	SO121	8580.5	77
406	SO122	8563.5	217
407	SO123	8546.5	357
408	SO124	8529.5	77
409	SO125	8512.5	217
410	SO126	8495.5	357
411	SO127	8478.5	77
412	SO128	8461.5	217
413	SO129	8444.5	357
414	SO130	8427.5	77
415	SO131	8410.5	217
416	SO132	8393.5	357
417	SO133	8376.5	77
418	SO134	8359.5	217
419	SO135	8342.5	357
420	SO136	8325.5	77
421	SO137	8308.5	217
422	SO138	8291.5	357
423	SO139	8274.5	77
424	SO140	8257.5	217
425	SO141	8240.5	357
426	SO142	8223.5	77
427	SO143	8206.5	217
428	SO144	8189.5	357
429	SO145	8172.5	77
430	SO146	8155.5	217
431	SO147	8138.5	357
432	SO148	8121.5	77
433	SO149	8104.5	217
434	SO150	8087.5	357
435	SO151	8070.5	77
436	SO152	8053.5	217
437	SO153	8036.5	357
438	SO154	8019.5	77
439	SO155	8002.5	217
440	SO156	7985.5	357
441	SO157	7968.5	77
442	SO158	7951.5	217
443	SO159	7934.5	357
444	SO160	7917.5	77
445	SO161	7900.5	217
446	SO162	7883.5	357
447	SO163	7866.5	77
448	SO164	7849.5	217
449	SO165	7832.5	357
450	SO166	7815.5	77

PAD NO.	PAD NAME	X	Y
451	SO167	7798.5	217
452	SO168	7781.5	357
453	SO169	7764.5	77
454	SO170	7747.5	217
455	SO171	7730.5	357
456	SO172	7713.5	77
457	SO173	7696.5	217
458	SO174	7679.5	357
459	SO175	7662.5	77
460	SO176	7645.5	217
461	SO177	7628.5	357
462	SO178	7611.5	77
463	SO179	7594.5	217
464	SO180	7577.5	357
465	SO181	7560.5	77
466	SO182	7543.5	217
467	SO183	7526.5	357
468	SO184	7509.5	77
469	SO185	7492.5	217
470	SO186	7475.5	357
471	SO187	7458.5	77
472	SO188	7441.5	217
473	SO189	7424.5	357
474	SO190	7407.5	77
475	SO191	7390.5	217
476	SO192	7373.5	357
477	SO193	7356.5	77
478	SO194	7339.5	217
479	SO195	7322.5	357
480	SO196	7305.5	77
481	SO197	7288.5	217
482	SO198	7271.5	357
483	SO199	7254.5	77
484	SO200	7237.5	217
485	SO201	7220.5	357
486	SO202	7203.5	77
487	SO203	7186.5	217
488	SO204	7169.5	357
489	SO205	7152.5	77
490	SO206	7135.5	217
491	SO207	7118.5	357
492	SO208	7101.5	77
493	SO209	7084.5	217
494	SO210	7067.5	357
495	SO211	7050.5	77
496	SO212	7033.5	217
497	SO213	7016.5	357
498	SO214	6999.5	77
499	SO215	6982.5	217
500	SO216	6965.5	357
501	SO217	6948.5	77
502	SO218	6931.5	217
503	SO219	6914.5	357
504	SO220	6897.5	77
505	SO221	6880.5	217
506	SO222	6863.5	357
507	SO223	6846.5	77



PAD NO.	PAD NAME	X	Y
508	SO224	6829.5	217
509	SO225	6812.5	357
510	SO226	6795.5	77
511	SO227	6778.5	217
512	SO228	6761.5	357
513	SO229	6744.5	77
514	SO230	6727.5	217
515	SO231	6710.5	357
516	SO232	6693.5	77
517	SO233	6676.5	217
518	SO234	6659.5	357
519	SO235	6642.5	77
520	SO236	6625.5	217
521	SO237	6608.5	357
522	SO238	6591.5	77
523	SO239	6574.5	217
524	SO240	6557.5	357
525	SO241	6540.5	77
526	SO242	6523.5	217
527	SO243	6506.5	357
528	SO244	6489.5	77
529	SO245	6472.5	217
530	SO246	6455.5	357
531	SO247	6438.5	77
532	SO248	6421.5	217
533	SO249	6404.5	357
534	SO250	6387.5	77
535	SO251	6370.5	217
536	SO252	6353.5	357
537	SO253	6336.5	77
538	SO254	6319.5	217
539	SO255	6302.5	357
540	SO256	6285.5	77
541	SO257	6268.5	217
542	SO258	6251.5	357
543	SO259	6234.5	77
544	SO260	6217.5	217
545	SO261	6200.5	357
546	SO262	6183.5	77
547	SO263	6166.5	217
548	SO264	6149.5	357
549	SO265	6132.5	77
550	SO266	6115.5	217
551	SO267	6098.5	357
552	SO268	6081.5	77
553	SO269	6064.5	217
554	SO270	6047.5	357
555	SO271	6030.5	77
556	SO272	6013.5	217
557	SO273	5996.5	357
558	SO274	5979.5	77
559	SO275	5962.5	217
560	SO276	5945.5	357
561	SO277	5928.5	77
562	SO278	5911.5	217
563	SO279	5894.5	357
564	SO280	5877.5	77

PAD NO.	PAD NAME	X	Y
565	SO281	5860.5	217
566	SO282	5843.5	357
567	SO283	5826.5	77
568	SO284	5809.5	217
569	SO285	5792.5	357
570	SO286	5775.5	77
571	SO287	5758.5	217
572	SO288	5741.5	357
573	SO289	5724.5	77
574	SO290	5707.5	217
575	SO291	5690.5	357
576	SO292	5673.5	77
577	SO293	5656.5	217
578	SO294	5639.5	357
579	SO295	5622.5	77
580	SO296	5605.5	217
581	SO297	5588.5	357
582	SO298	5571.5	77
583	SO299	5554.5	217
584	SO300	5537.5	357
585	SO301	5520.5	77
586	SO302	5503.5	217
587	SO303	5486.5	357
588	SO304	5469.5	77
589	SO305	5452.5	217
590	SO306	5435.5	357
591	SO307	5418.5	77
592	SO308	5401.5	217
593	SO309	5384.5	357
594	SO310	5367.5	77
595	SO311	5350.5	217
596	SO312	5333.5	357
597	SO313	5316.5	77
598	SO314	5299.5	217
599	SO315	5282.5	357
600	SO316	5265.5	77
601	SO317	5248.5	217
602	SO318	5231.5	357
603	SO319	5214.5	77
604	SO320	5197.5	217
605	SO321	5180.5	357
606	SO322	5163.5	77
607	SO323	5146.5	217
608	SO324	5129.5	357
609	SO325	5112.5	77
610	SO326	5095.5	217
611	SO327	5078.5	357
612	SO328	5061.5	77
613	SO329	5044.5	217
614	SO330	5027.5	357
615	SO331	5010.5	77
616	SO332	4993.5	217
617	SO333	4976.5	357
618	SO334	4959.5	77
619	SO335	4942.5	217
620	SO336	4925.5	357
621	SO337	4908.5	77

PAD NO.	PAD NAME	X	Y
622	SO338	4891.5	217
623	SO339	4874.5	357
624	SO340	4857.5	77
625	SO341	4840.5	217
626	SO342	4823.5	357
627	SO343	4806.5	77
628	SO344	4789.5	217
629	SO345	4772.5	357
630	SO346	4755.5	77
631	SO347	4738.5	217
632	SO348	4721.5	357
633	SO349	4704.5	77
634	SO350	4687.5	217
635	SO351	4670.5	357
636	SO352	4653.5	77
637	SO353	4636.5	217
638	SO354	4619.5	357
639	SO355	4602.5	77
640	SO356	4585.5	217
641	SO357	4568.5	357
642	SO358	4551.5	77
643	SO359	4534.5	217
644	SO360	4517.5	357
645	SO361	4500.5	77
646	SO362	4483.5	217
647	SO363	4466.5	357
648	SO364	4449.5	77
649	SO365	4432.5	217
650	SO366	4415.5	357
651	SO367	4398.5	77
652	SO368	4381.5	217
653	SO369	4364.5	357
654	SO370	4347.5	77
655	SO371	4330.5	217
656	SO372	4313.5	357
657	SO373	4296.5	77
658	SO374	4279.5	217
659	SO375	4262.5	357
660	SO376	4245.5	77
661	SO377	4228.5	217
662	SO378	4211.5	357
663	SO379	4194.5	77
664	SO380	4177.5	217
665	SO381	4160.5	357
666	SO382	4143.5	77
667	SO383	4126.5	217
668	SO384	4109.5	357
669	SO385	4092.5	77
670	SO386	4075.5	217
671	SO387	4058.5	357
672	SO388	4041.5	77
673	SO389	4024.5	217
674	SO390	4007.5	357
675	SO391	3990.5	77
676	SO392	3973.5	217
677	SO393	3956.5	357
678	SO394	3939.5	77

PAD NO.	PAD NAME	X	Y
679	SO395	3922.5	217
680	SO396	3905.5	357
681	SO397	3888.5	77
682	SO398	3871.5	217
683	SO399	3854.5	357
684	SO400	3837.5	77
685	SO401	3820.5	217
686	SO402	3803.5	357
687	SO403	3786.5	77
688	SO404	3769.5	217
689	SO405	3752.5	357
690	SO406	3735.5	77
691	SO407	3718.5	217
692	SO408	3701.5	357
693	SO409	3684.5	77
694	SO410	3667.5	217
695	SO411	3650.5	357
696	SO412	3633.5	77
697	SO413	3616.5	217
698	SO414	3599.5	357
699	SO415	3582.5	77
700	SO416	3565.5	217
701	SO417	3548.5	357
702	SO418	3531.5	77
703	SO419	3514.5	217
704	SO420	3497.5	357
705	SO421	3480.5	77
706	SO422	3463.5	217
707	SO423	3446.5	357
708	SO424	3429.5	77
709	SO425	3412.5	217
710	SO426	3395.5	357
711	SO427	3378.5	77
712	SO428	3361.5	217
713	SO429	3344.5	357
714	SO430	3327.5	77
715	SO431	3310.5	217
716	SO432	3293.5	357
717	SO433	3276.5	77
718	SO434	3259.5	217
719	SO435	3242.5	357
720	SO436	3225.5	77
721	SO437	3208.5	217
722	SO438	3191.5	357
723	SO439	3174.5	77
724	SO440	3157.5	217
725	SO441	3140.5	357
726	SO442	3123.5	77
727	SO443	3106.5	217
728	SO444	3089.5	357
729	SO445	3072.5	77
730	SO446	3055.5	217
731	SO447	3038.5	357
732	SO448	3021.5	77
733	SO449	3004.5	217
734	SO450	2987.5	357
735	SO451	2970.5	77

PAD NO.	PAD NAME	X	Y
736	SO452	2953.5	217
737	SO453	2936.5	357
738	SO454	2919.5	77
739	SO455	2902.5	217
740	SO456	2885.5	357
741	SO457	2868.5	77
742	SO458	2851.5	217
743	SO459	2834.5	357
744	SO460	2817.5	77
745	SO461	2800.5	217
746	SO462	2783.5	357
747	SO463	2766.5	77
748	SO464	2749.5	217
749	SO465	2732.5	357
750	SO466	2715.5	77
751	SO467	2698.5	217
752	SO468	2681.5	357
753	SO469	2664.5	77
754	SO470	2647.5	217
755	SO471	2630.5	357
756	SO472	2613.5	77
757	SO473	2596.5	217
758	SO474	2579.5	357
759	SO475	2562.5	77
760	SO476	2545.5	217
761	SO477	2528.5	357
762	SO478	2511.5	77
763	SO479	2494.5	217
764	SO480	2477.5	357
765	SO481	2460.5	77
766	SO482	2443.5	217
767	SO483	2426.5	357
768	SO484	2409.5	77
769	SO485	2392.5	217
770	SO486	2375.5	357
771	SO487	2358.5	77
772	SO488	2341.5	217
773	SO489	2324.5	357
774	SO490	2307.5	77
775	SO491	2290.5	217
776	SO492	2273.5	357
777	SO493	2256.5	77
778	SO494	2239.5	217
779	SO495	2222.5	357
780	SO496	2205.5	77
781	SO497	2188.5	217
782	SO498	2171.5	357
783	SO499	2154.5	77
784	SO500	2137.5	217
785	SO501	2120.5	357
786	SO502	2103.5	77
787	SO503	2086.5	217
788	SO504	2069.5	357
789	SO505	2052.5	77
790	SO506	2035.5	217
791	SO507	2018.5	357
792	SO508	2001.5	77

PAD NO.	PAD NAME	X	Y
793	SO509	1984.5	217
794	SO510	1967.5	357
795	SO511	1950.5	77
796	SO512	1933.5	217
797	SO513	1916.5	357
798	SO514	1899.5	77
799	SO515	1882.5	217
800	SO516	1865.5	357
801	SO517	1848.5	77
802	SO518	1831.5	217
803	SO519	1814.5	357
804	SO520	1797.5	77
805	SO521	1780.5	217
806	SO522	1763.5	357
807	SO523	1746.5	77
808	SO524	1729.5	217
809	SO525	1712.5	357
810	SO526	1695.5	77
811	SO527	1678.5	217
812	SO528	1661.5	357
813	SO529	1644.5	77
814	SO530	1627.5	217
815	SO531	1610.5	357
816	SO532	1593.5	77
817	SO533	1576.5	217
818	SO534	1559.5	357
819	SO535	1542.5	77
820	SO536	1525.5	217
821	SO537	1508.5	357
822	SO538	1491.5	77
823	SO539	1474.5	217
824	SO540	1457.5	357
825	SO541	1440.5	77
826	SO542	1423.5	217
827	SO543	1406.5	357
828	SO544	1389.5	77
829	SO545	1372.5	217
830	SO546	1355.5	357
831	SO547	1338.5	77
832	SO548	1321.5	217
833	SO549	1304.5	357
834	SO550	1287.5	77
835	SO551	1270.5	217
836	SO552	1253.5	357
837	SO553	1236.5	77
838	SO554	1219.5	217
839	SO555	1202.5	357
840	SO556	1185.5	77
841	SO557	1168.5	217
842	SO558	1151.5	357
843	SO559	1134.5	77
844	SO560	1117.5	217
845	SO561	1100.5	357
846	SO562	1083.5	77
847	SO563	1066.5	217
848	SO564	1049.5	357
849	SO565	1032.5	77

PAD NO.	PAD NAME	X	Y
850	SO566	1015.5	217
851	SO567	998.5	357
852	SO568	981.5	77
853	SO569	964.5	217
854	SO570	947.5	357
855	SO571	930.5	77
856	SO572	913.5	217
857	SO573	896.5	357
858	SO574	879.5	77
859	SO575	862.5	217
860	SO576	845.5	357
861	SO577	828.5	77
862	SO578	811.5	217
863	SO579	794.5	357
864	SO580	777.5	77
865	SO581	760.5	217
866	SO582	743.5	357
867	SO583	726.5	77
868	SO584	709.5	217
869	SO585	692.5	357
870	SO586	675.5	77
871	SO587	658.5	217
872	SO588	641.5	357
873	SO589	624.5	77
874	SO590	607.5	217
875	SO591	590.5	357
876	SO592	573.5	77
877	SO593	556.5	217
878	SO594	539.5	357
879	SO595	522.5	77
880	SO596	505.5	217
881	SO597	488.5	357
882	SO598	471.5	77
883	SO599	454.5	217
884	SO600	437.5	357
885	SHIELDING1	403.5	357
886	SHIELDING2	369.5	357
887	SHIELDING3	335.5	357
888	SHIELDING4	301.5	357
889	SHIELDING5	267.5	357
890	SHIELDING6	233.5	357
891	SHIELDING7	-233.5	357
892	SHIELDING8	-267.5	357
893	SHIELDING9	-301.5	357
894	SHIELDING10	-335.5	357
895	SHIELDING11	-369.5	357
896	SHIELDING12	-403.5	357
897	SO601	-437.5	357
898	SO602	-454.5	217
899	SO603	-471.5	77
900	SO604	-488.5	357
901	SO605	-505.5	217
902	SO606	-522.5	77
903	SO607	-539.5	357
904	SO608	-556.5	217
905	SO609	-573.5	77
906	SO610	-590.5	357

PAD NO.	PAD NAME	X	Y
907	SO611	-607.5	217
908	SO612	-624.5	77
909	SO613	-641.5	357
910	SO614	-658.5	217
911	SO615	-675.5	77
912	SO616	-692.5	357
913	SO617	-709.5	217
914	SO618	-726.5	77
915	SO619	-743.5	357
916	SO620	-760.5	217
917	SO621	-777.5	77
918	SO622	-794.5	357
919	SO623	-811.5	217
920	SO624	-828.5	77
921	SO625	-845.5	357
922	SO626	-862.5	217
923	SO627	-879.5	77
924	SO628	-896.5	357
925	SO629	-913.5	217
926	SO630	-930.5	77
927	SO631	-947.5	357
928	SO632	-964.5	217
929	SO633	-981.5	77
930	SO634	-998.5	357
931	SO635	-1015.5	217
932	SO636	-1032.5	77
933	SO637	-1049.5	357
934	SO638	-1066.5	217
935	SO639	-1083.5	77
936	SO640	-1100.5	357
937	SO641	-1117.5	217
938	SO642	-1134.5	77
939	SO643	-1151.5	357
940	SO644	-1168.5	217
941	SO645	-1185.5	77
942	SO646	-1202.5	357
943	SO647	-1219.5	217
944	SO648	-1236.5	77
945	SO649	-1253.5	357
946	SO650	-1270.5	217
947	SO651	-1287.5	77
948	SO652	-1304.5	357
949	SO653	-1321.5	217
950	SO654	-1338.5	77
951	SO655	-1355.5	357
952	SO656	-1372.5	217
953	SO657	-1389.5	77
954	SO658	-1406.5	357
955	SO659	-1423.5	217
956	SO660	-1440.5	77
957	SO661	-1457.5	357
958	SO662	-1474.5	217
959	SO663	-1491.5	77
960	SO664	-1508.5	357
961	SO665	-1525.5	217
962	SO666	-1542.5	77
963	SO667	-1559.5	357

PAD NO.	PAD NAME	X	Y
964	SO668	-1576.5	217
965	SO669	-1593.5	77
966	SO670	-1610.5	357
967	SO671	-1627.5	217
968	SO672	-1644.5	77
969	SO673	-1661.5	357
970	SO674	-1678.5	217
971	SO675	-1695.5	77
972	SO676	-1712.5	357
973	SO677	-1729.5	217
974	SO678	-1746.5	77
975	SO679	-1763.5	357
976	SO680	-1780.5	217
977	SO681	-1797.5	77
978	SO682	-1814.5	357
979	SO683	-1831.5	217
980	SO684	-1848.5	77
981	SO685	-1865.5	357
982	SO686	-1882.5	217
983	SO687	-1899.5	77
984	SO688	-1916.5	357
985	SO689	-1933.5	217
986	SO690	-1950.5	77
987	SO691	-1967.5	357
988	SO692	-1984.5	217
989	SO693	-2001.5	77
990	SO694	-2018.5	357
991	SO695	-2035.5	217
992	SO696	-2052.5	77
993	SO697	-2069.5	357
994	SO698	-2086.5	217
995	SO699	-2103.5	77
996	SO700	-2120.5	357
997	SO701	-2137.5	217
998	SO702	-2154.5	77
999	SO703	-2171.5	357
1000	SO704	-2188.5	217
1001	SO705	-2205.5	77
1002	SO706	-2222.5	357
1003	SO707	-2239.5	217
1004	SO708	-2256.5	77
1005	SO709	-2273.5	357
1006	SO710	-2290.5	217
1007	SO711	-2307.5	77
1008	SO712	-2324.5	357
1009	SO713	-2341.5	217
1010	SO714	-2358.5	77
1011	SO715	-2375.5	357
1012	SO716	-2392.5	217
1013	SO717	-2409.5	77
1014	SO718	-2426.5	357
1015	SO719	-2443.5	217
1016	SO720	-2460.5	77
1017	SO721	-2477.5	357
1018	SO722	-2494.5	217
1019	SO723	-2511.5	77
1020	SO724	-2528.5	357

PAD NO.	PAD NAME	X	Y
1021	SO725	-2545.5	217
1022	SO726	-2562.5	77
1023	SO727	-2579.5	357
1024	SO728	-2596.5	217
1025	SO729	-2613.5	77
1026	SO730	-2630.5	357
1027	SO731	-2647.5	217
1028	SO732	-2664.5	77
1029	SO733	-2681.5	357
1030	SO734	-2698.5	217
1031	SO735	-2715.5	77
1032	SO736	-2732.5	357
1033	SO737	-2749.5	217
1034	SO738	-2766.5	77
1035	SO739	-2783.5	357
1036	SO740	-2800.5	217
1037	SO741	-2817.5	77
1038	SO742	-2834.5	357
1039	SO743	-2851.5	217
1040	SO744	-2868.5	77
1041	SO745	-2885.5	357
1042	SO746	-2902.5	217
1043	SO747	-2919.5	77
1044	SO748	-2936.5	357
1045	SO749	-2953.5	217
1046	SO750	-2970.5	77
1047	SO751	-2987.5	357
1048	SO752	-3004.5	217
1049	SO753	-3021.5	77
1050	SO754	-3038.5	357
1051	SO755	-3055.5	217
1052	SO756	-3072.5	77
1053	SO757	-3089.5	357
1054	SO758	-3106.5	217
1055	SO759	-3123.5	77
1056	SO760	-3140.5	357
1057	SO761	-3157.5	217
1058	SO762	-3174.5	77
1059	SO763	-3191.5	357
1060	SO764	-3208.5	217
1061	SO765	-3225.5	77
1062	SO766	-3242.5	357
1063	SO767	-3259.5	217
1064	SO768	-3276.5	77
1065	SO769	-3293.5	357
1066	SO770	-3310.5	217
1067	SO771	-3327.5	77
1068	SO772	-3344.5	357
1069	SO773	-3361.5	217
1070	SO774	-3378.5	77
1071	SO775	-3395.5	357
1072	SO776	-3412.5	217
1073	SO777	-3429.5	77
1074	SO778	-3446.5	357
1075	SO779	-3463.5	217
1076	SO780	-3480.5	77
1077	SO781	-3497.5	357

PAD NO.	PAD NAME	X	Y
1078	SO782	-3514.5	217
1079	SO783	-3531.5	77
1080	SO784	-3548.5	357
1081	SO785	-3565.5	217
1082	SO786	-3582.5	77
1083	SO787	-3599.5	357
1084	SO788	-3616.5	217
1085	SO789	-3633.5	77
1086	SO790	-3650.5	357
1087	SO791	-3667.5	217
1088	SO792	-3684.5	77
1089	SO793	-3701.5	357
1090	SO794	-3718.5	217
1091	SO795	-3735.5	77
1092	SO796	-3752.5	357
1093	SO797	-3769.5	217
1094	SO798	-3786.5	77
1095	SO799	-3803.5	357
1096	SO800	-3820.5	217
1097	SO801	-3837.5	77
1098	SO802	-3854.5	357
1099	SO803	-3871.5	217
1100	SO804	-3888.5	77
1101	SO805	-3905.5	357
1102	SO806	-3922.5	217
1103	SO807	-3939.5	77
1104	SO808	-3956.5	357
1105	SO809	-3973.5	217
1106	SO810	-3990.5	77
1107	SO811	-4007.5	357
1108	SO812	-4024.5	217
1109	SO813	-4041.5	77
1110	SO814	-4058.5	357
1111	SO815	-4075.5	217
1112	SO816	-4092.5	77
1113	SO817	-4109.5	357
1114	SO818	-4126.5	217
1115	SO819	-4143.5	77
1116	SO820	-4160.5	357
1117	SO821	-4177.5	217
1118	SO822	-4194.5	77
1119	SO823	-4211.5	357
1120	SO824	-4228.5	217
1121	SO825	-4245.5	77
1122	SO826	-4262.5	357
1123	SO827	-4279.5	217
1124	SO828	-4296.5	77
1125	SO829	-4313.5	357
1126	SO830	-4330.5	217
1127	SO831	-4347.5	77
1128	SO832	-4364.5	357
1129	SO833	-4381.5	217
1130	SO834	-4398.5	77
1131	SO835	-4415.5	357
1132	SO836	-4432.5	217
1133	SO837	-4449.5	77
1134	SO838	-4466.5	357

PAD NO.	PAD NAME	X	Y
1135	SO839	-4483.5	217
1136	SO840	-4500.5	77
1137	SO841	-4517.5	357
1138	SO842	-4534.5	217
1139	SO843	-4551.5	77
1140	SO844	-4568.5	357
1141	SO845	-4585.5	217
1142	SO846	-4602.5	77
1143	SO847	-4619.5	357
1144	SO848	-4636.5	217
1145	SO849	-4653.5	77
1146	SO850	-4670.5	357
1147	SO851	-4687.5	217
1148	SO852	-4704.5	77
1149	SO853	-4721.5	357
1150	SO854	-4738.5	217
1151	SO855	-4755.5	77
1152	SO856	-4772.5	357
1153	SO857	-4789.5	217
1154	SO858	-4806.5	77
1155	SO859	-4823.5	357
1156	SO860	-4840.5	217
1157	SO861	-4857.5	77
1158	SO862	-4874.5	357
1159	SO863	-4891.5	217
1160	SO864	-4908.5	77
1161	SO865	-4925.5	357
1162	SO866	-4942.5	217
1163	SO867	-4959.5	77
1164	SO868	-4976.5	357
1165	SO869	-4993.5	217
1166	SO870	-5010.5	77
1167	SO871	-5027.5	357
1168	SO872	-5044.5	217
1169	SO873	-5061.5	77
1170	SO874	-5078.5	357
1171	SO875	-5095.5	217
1172	SO876	-5112.5	77
1173	SO877	-5129.5	357
1174	SO878	-5146.5	217
1175	SO879	-5163.5	77
1176	SO880	-5180.5	357
1177	SO881	-5197.5	217
1178	SO882	-5214.5	77
1179	SO883	-5231.5	357
1180	SO884	-5248.5	217
1181	SO885	-5265.5	77
1182	SO886	-5282.5	357
1183	SO887	-5299.5	217
1184	SO888	-5316.5	77
1185	SO889	-5333.5	357
1186	SO890	-5350.5	217
1187	SO891	-5367.5	77
1188	SO892	-5384.5	357
1189	SO893	-5401.5	217
1190	SO894	-5418.5	77
1191	SO895	-5435.5	357

PAD NO.	PAD NAME	X	Y
1192	SO896	-5452.5	217
1193	SO897	-5469.5	77
1194	SO898	-5486.5	357
1195	SO899	-5503.5	217
1196	SO900	-5520.5	77
1197	SO901	-5537.5	357
1198	SO902	-5554.5	217
1199	SO903	-5571.5	77
1200	SO904	-5588.5	357
1201	SO905	-5605.5	217
1202	SO906	-5622.5	77
1203	SO907	-5639.5	357
1204	SO908	-5656.5	217
1205	SO909	-5673.5	77
1206	SO910	-5690.5	357
1207	SO911	-5707.5	217
1208	SO912	-5724.5	77
1209	SO913	-5741.5	357
1210	SO914	-5758.5	217
1211	SO915	-5775.5	77
1212	SO916	-5792.5	357
1213	SO917	-5809.5	217
1214	SO918	-5826.5	77
1215	SO919	-5843.5	357
1216	SO920	-5860.5	217
1217	SO921	-5877.5	77
1218	SO922	-5894.5	357
1219	SO923	-5911.5	217
1220	SO924	-5928.5	77
1221	SO925	-5945.5	357
1222	SO926	-5962.5	217
1223	SO927	-5979.5	77
1224	SO928	-5996.5	357
1225	SO929	-6013.5	217
1226	SO930	-6030.5	77
1227	SO931	-6047.5	357
1228	SO932	-6064.5	217
1229	SO933	-6081.5	77
1230	SO934	-6098.5	357
1231	SO935	-6115.5	217
1232	SO936	-6132.5	77
1233	SO937	-6149.5	357
1234	SO938	-6166.5	217
1235	SO939	-6183.5	77
1236	SO940	-6200.5	357
1237	SO941	-6217.5	217
1238	SO942	-6234.5	77
1239	SO943	-6251.5	357
1240	SO944	-6268.5	217
1241	SO945	-6285.5	77
1242	SO946	-6302.5	357
1243	SO947	-6319.5	217
1244	SO948	-6336.5	77
1245	SO949	-6353.5	357
1246	SO950	-6370.5	217
1247	SO951	-6387.5	77
1248	SO952	-6404.5	357

PAD NO.	PAD NAME	X	Y
1249	SO953	-6421.5	217
1250	SO954	-6438.5	77
1251	SO955	-6455.5	357
1252	SO956	-6472.5	217
1253	SO957	-6489.5	77
1254	SO958	-6506.5	357
1255	SO959	-6523.5	217
1256	SO960	-6540.5	77
1257	SO961	-6557.5	357
1258	SO962	-6574.5	217
1259	SO963	-6591.5	77
1260	SO964	-6608.5	357
1261	SO965	-6625.5	217
1262	SO966	-6642.5	77
1263	SO967	-6659.5	357
1264	SO968	-6676.5	217
1265	SO969	-6693.5	77
1266	SO970	-6710.5	357
1267	SO971	-6727.5	217
1268	SO972	-6744.5	77
1269	SO973	-6761.5	357
1270	SO974	-6778.5	217
1271	SO975	-6795.5	77
1272	SO976	-6812.5	357
1273	SO977	-6829.5	217
1274	SO978	-6846.5	77
1275	SO979	-6863.5	357
1276	SO980	-6880.5	217
1277	SO981	-6897.5	77
1278	SO982	-6914.5	357
1279	SO983	-6931.5	217
1280	SO984	-6948.5	77
1281	SO985	-6965.5	357
1282	SO986	-6982.5	217
1283	SO987	-6999.5	77
1284	SO988	-7016.5	357
1285	SO989	-7033.5	217
1286	SO990	-7050.5	77
1287	SO991	-7067.5	357
1288	SO992	-7084.5	217
1289	SO993	-7101.5	77
1290	SO994	-7118.5	357
1291	SO995	-7135.5	217
1292	SO996	-7152.5	77
1293	SO997	-7169.5	357
1294	SO998	-7186.5	217
1295	SO999	-7203.5	77
1296	SO1000	-7220.5	357
1297	SO1001	-7237.5	217
1298	SO1002	-7254.5	77
1299	SO1003	-7271.5	357
1300	SO1004	-7288.5	217
1301	SO1005	-7305.5	77
1302	SO1006	-7322.5	357
1303	SO1007	-7339.5	217
1304	SO1008	-7356.5	77
1305	SO1009	-7373.5	357

PAD NO.	PAD NAME	X	Y
1306	SO1010	-7390.5	217
1307	SO1011	-7407.5	77
1308	SO1012	-7424.5	357
1309	SO1013	-7441.5	217
1310	SO1014	-7458.5	77
1311	SO1015	-7475.5	357
1312	SO1016	-7492.5	217
1313	SO1017	-7509.5	77
1314	SO1018	-7526.5	357
1315	SO1019	-7543.5	217
1316	SO1020	-7560.5	77
1317	SO1021	-7577.5	357
1318	SO1022	-7594.5	217
1319	SO1023	-7611.5	77
1320	SO1024	-7628.5	357
1321	SO1025	-7645.5	217
1322	SO1026	-7662.5	77
1323	SO1027	-7679.5	357
1324	SO1028	-7696.5	217
1325	SO1029	-7713.5	77
1326	SO1030	-7730.5	357
1327	SO1031	-7747.5	217
1328	SO1032	-7764.5	77
1329	SO1033	-7781.5	357
1330	SO1034	-7798.5	217
1331	SO1035	-7815.5	77
1332	SO1036	-7832.5	357
1333	SO1037	-7849.5	217
1334	SO1038	-7866.5	77
1335	SO1039	-7883.5	357
1336	SO1040	-7900.5	217
1337	SO1041	-7917.5	77
1338	SO1042	-7934.5	357
1339	SO1043	-7951.5	217
1340	SO1044	-7968.5	77
1341	SO1045	-7985.5	357
1342	SO1046	-8002.5	217
1343	SO1047	-8019.5	77
1344	SO1048	-8036.5	357
1345	SO1049	-8053.5	217
1346	SO1050	-8070.5	77
1347	SO1051	-8087.5	357
1348	SO1052	-8104.5	217
1349	SO1053	-8121.5	77
1350	SO1054	-8138.5	357
1351	SO1055	-8155.5	217
1352	SO1056	-8172.5	77
1353	SO1057	-8189.5	357
1354	SO1058	-8206.5	217
1355	SO1059	-8223.5	77
1356	SO1060	-8240.5	357
1357	SO1061	-8257.5	217
1358	SO1062	-8274.5	77
1359	SO1063	-8291.5	357
1360	SO1064	-8308.5	217
1361	SO1065	-8325.5	77
1362	SO1066	-8342.5	357

PAD NO.	PAD NAME	X	Y
1363	SO1067	-8359.5	217
1364	SO1068	-8376.5	77
1365	SO1069	-8393.5	357
1366	SO1070	-8410.5	217
1367	SO1071	-8427.5	77
1368	SO1072	-8444.5	357
1369	SO1073	-8461.5	217
1370	SO1074	-8478.5	77
1371	SO1075	-8495.5	357
1372	SO1076	-8512.5	217
1373	SO1077	-8529.5	77
1374	SO1078	-8546.5	357
1375	SO1079	-8563.5	217
1376	SO1080	-8580.5	77
1377	SO1081	-8597.5	357
1378	SO1082	-8614.5	217
1379	SO1083	-8631.5	77
1380	SO1084	-8648.5	357
1381	SO1085	-8665.5	217
1382	SO1086	-8682.5	77
1383	SO1087	-8699.5	357
1384	SO1088	-8716.5	217
1385	SO1089	-8733.5	77
1386	SO1090	-8750.5	357
1387	SO1091	-8767.5	217
1388	SO1092	-8784.5	77
1389	SO1093	-8801.5	357
1390	SO1094	-8818.5	217
1391	SO1095	-8835.5	77
1392	SO1096	-8852.5	357
1393	SO1097	-8869.5	217
1394	SO1098	-8886.5	77
1395	SO1099	-8903.5	357
1396	SO1100	-8920.5	217
1397	SO1101	-8937.5	77
1398	SO1102	-8954.5	357
1399	SO1103	-8971.5	217
1400	SO1104	-8988.5	77
1401	SO1105	-9005.5	357
1402	SO1106	-9022.5	217
1403	SO1107	-9039.5	77
1404	SO1108	-9056.5	357
1405	SO1109	-9073.5	217
1406	SO1110	-9090.5	77
1407	SO1111	-9107.5	357
1408	SO1112	-9124.5	217
1409	SO1113	-9141.5	77
1410	SO1114	-9158.5	357
1411	SO1115	-9175.5	217
1412	SO1116	-9192.5	77
1413	SO1117	-9209.5	357
1414	SO1118	-9226.5	217
1415	SO1119	-9243.5	77
1416	SO1120	-9260.5	357

PAD NO.	PAD NAME	X	Y
1417	SO1121	-9277.5	217
1418	SO1122	-9294.5	77
1419	SO1123	-9311.5	357
1420	SO1124	-9328.5	217
1421	SO1125	-9345.5	77
1422	SO1126	-9362.5	357
1423	SO1127	-9379.5	217
1424	SO1128	-9396.5	77
1425	SO1129	-9413.5	357
1426	SO1130	-9430.5	217
1427	SO1131	-9447.5	77
1428	SO1132	-9464.5	357
1429	SO1133	-9481.5	217
1430	SO1134	-9498.5	77
1431	SO1135	-9515.5	357
1432	SO1136	-9532.5	217
1433	SO1137	-9549.5	77
1434	SO1138	-9566.5	357
1435	SO1139	-9583.5	217
1436	SO1140	-9600.5	77
1437	SO1141	-9617.5	357
1438	SO1142	-9634.5	217
1439	SO1143	-9651.5	77
1440	SO1144	-9668.5	357
1441	SO1145	-9685.5	217
1442	SO1146	-9702.5	77
1443	SO1147	-9719.5	357
1444	SO1148	-9736.5	217
1445	SO1149	-9753.5	77
1446	SO1150	-9770.5	357
1447	SO1151	-9787.5	217
1448	SO1152	-9804.5	77
1449	SO1153	-9821.5	357
1450	SO1154	-9838.5	217
1451	SO1155	-9855.5	77
1452	SO1156	-9872.5	357
1453	SO1157	-9889.5	217
1454	SO1158	-9906.5	77
1455	SO1159	-9923.5	357
1456	SO1160	-9940.5	217
1457	SO1161	-9957.5	77
1458	SO1162	-9974.5	357
1459	SO1163	-9991.5	217
1460	SO1164	-10008.5	77
1461	SO1165	-10025.5	357
1462	SO1166	-10042.5	217
1463	SO1167	-10059.5	77
1464	SO1168	-10076.5	357
1465	SO1169	-10093.5	217
1466	SO1170	-10110.5	77
1467	SO1171	-10127.5	357
1468	SO1172	-10144.5	217
1469	SO1173	-10161.5	77
1470	SO1174	-10178.5	357

PAD NO.	PAD NAME	X	Y
1471	SO1175	-10195.5	217
1472	SO1176	-10212.5	77
1473	SO1177	-10229.5	357
1474	SO1178	-10246.5	217
1475	SO1179	-10263.5	77
1476	SO1180	-10280.5	357
1477	SO1181	-10297.5	217
1478	SO1182	-10314.5	77
1479	SO1183	-10331.5	357
1480	SO1184	-10348.5	217
1481	SO1185	-10365.5	77
1482	SO1186	-10382.5	357
1483	SO1187	-10399.5	217
1484	SO1188	-10416.5	77
1485	SO1189	-10433.5	357
1486	SO1190	-10450.5	217
1487	SO1191	-10467.5	77
1488	SO1192	-10484.5	357
1489	SO1193	-10501.5	217
1490	SO1194	-10518.5	77
1491	SO1195	-10535.5	357
1492	SO1196	-10552.5	217
1493	SO1197	-10569.5	77
1494	SO1198	-10586.5	357
1495	SO1199	-10603.5	217
1496	SO1200	-10620.5	77
1497	DUM_GND	-10664	377
1498	COM_PASSA	-10714	377
1499	COM_PASSA	-10764	377
1500	DUM_GND	-10814	377
1501	XON_R	-10864	377
1502	XON_R	-10914	377
1503	HSD_R	-11179	397
1504	DUM_GND	-11049	357
1505	OEV_R	-11179	317
1506	DUM_GND	-11049	277
1507	UD_R	-11179	237
1508	DUM_GND	-11049	197
1509	CKV_R	-11179	157
1510	DUM_GND	-11049	117
1511	STV1_R	-11179	77
1512	DUM_GND	-11049	37
1513	STV2_R	-11179	-3
1514	DUM_GND	-11049	-43
1515	STV1_R	-11179	-83
1516	DUM_GND	-11049	-123
1517	DUM_GND	-11179	-163
1518	DUM_GND	-11049	-203
1519	STBN_R	-11179	-243
1520	DUM_GND	-11049	-283
1521	DUM_GND	-11179	-323
1522	DUM_GND	-11049	-363

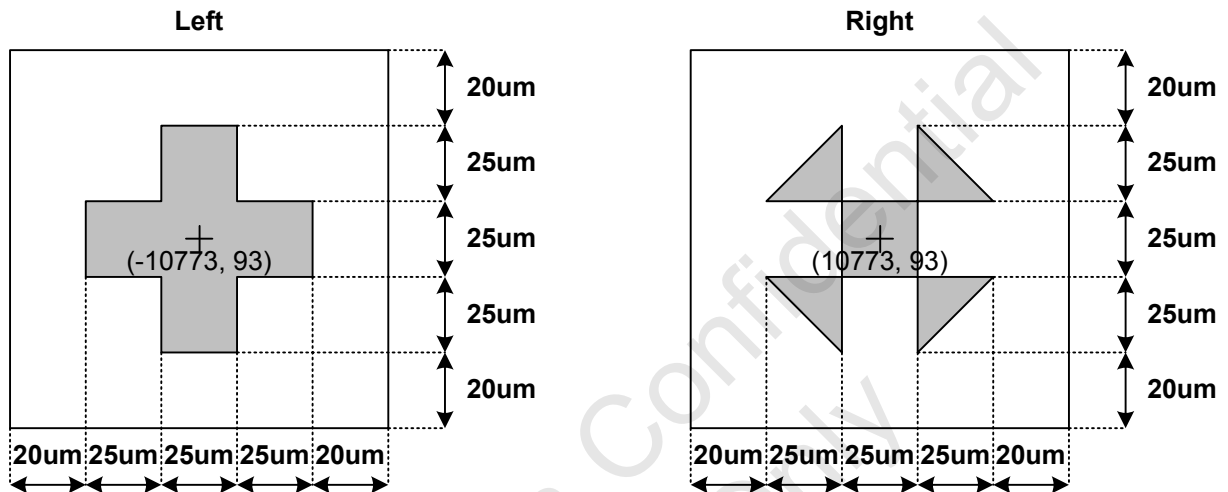
#### 7.4. Alignment Mark

--Alignment Mark coordinate

Left (-10773, 93)

Right (10773, 93)

--Alignment Mark size



---

## 8. DISCLAIMER

The information appearing in this publication is believed to be accurate.

Integrated circuits sold by ORISE Technology are covered by the warranty and patent indemnification provisions stipulated in the terms of sale only. ORISE Technology makes no warranty, express, statutory implied or by description regarding the information in this publication or regarding the freedom of the described chip(s) from patent infringement. FURTHERMORE, ORISE Technology MAKES NO WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PURPOSE. ORISE Technology reserves the right to halt production or alter the specifications and prices at any time without notice. Accordingly, the reader is cautioned to verify that the data sheets and other information in this publication are current before placing orders. Products described herein are intended for use in normal commercial applications. Applications involving unusual environmental or reliability requirements, e.g. military equipment or medical life support equipment, are specifically not recommended without additional processing by ORISE Technology for such applications. Please note that application circuits illustrated in this document are for reference purposes only.

ORISE Tech Confidential  
For 橋電 Use Only



**9. REVISION HISTORY**

Date	Revision #	Description	Page
JUL. 07, 2010	0.3	1. Add gamma correction resistor ratio Table. 2. Add output voltage V.S. input data table	20 21-22
JUN. 07, 2010	0.2	1. Modify 4.1 PAD description DGEN default value. OTPEN pin replace to PWM_IN 2. Modify 7.3 PAD location OTP_WEN and PWM_IN coordinate.	5-7 30
DEC. 25, 2009	0.1	Original	41

ORISE Tech Confidential  
For 橋電 Use Only