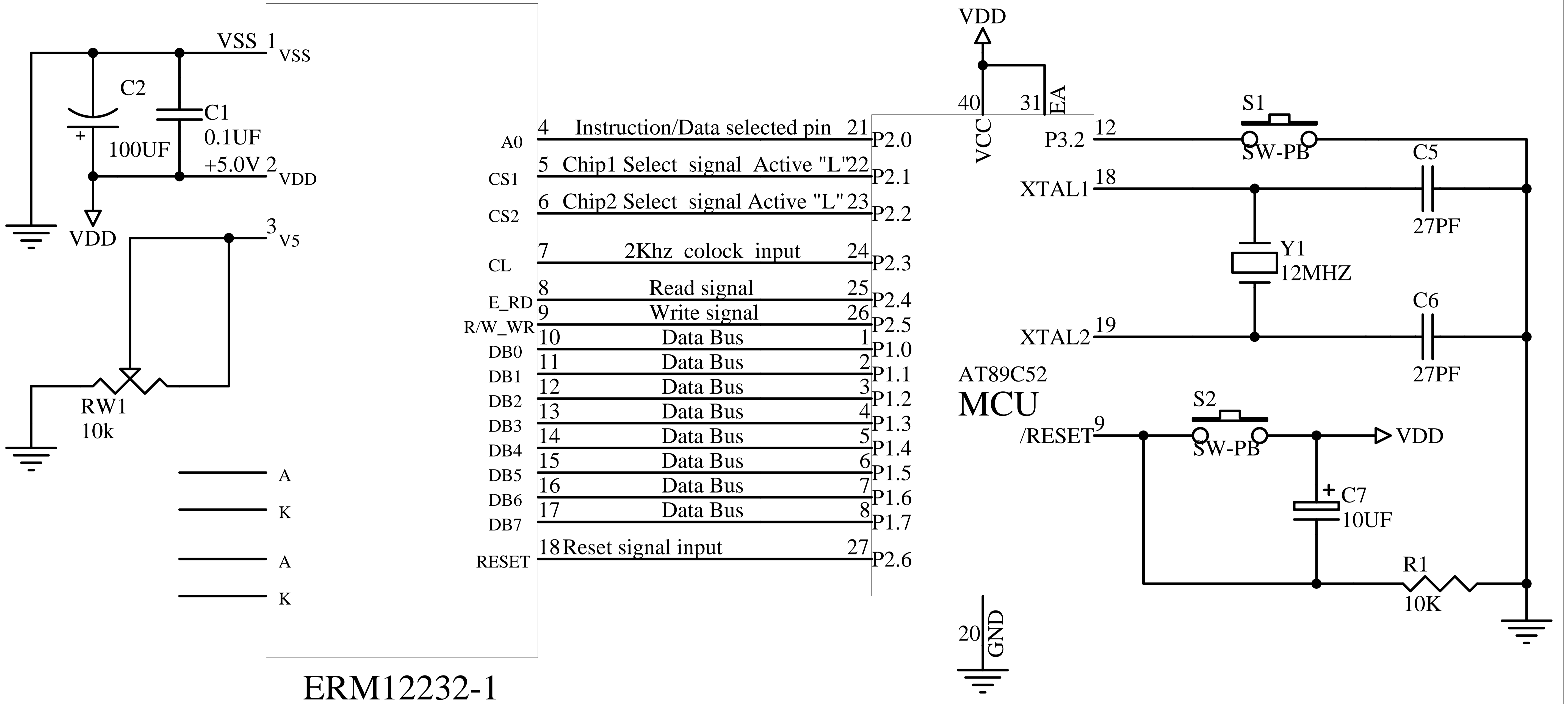


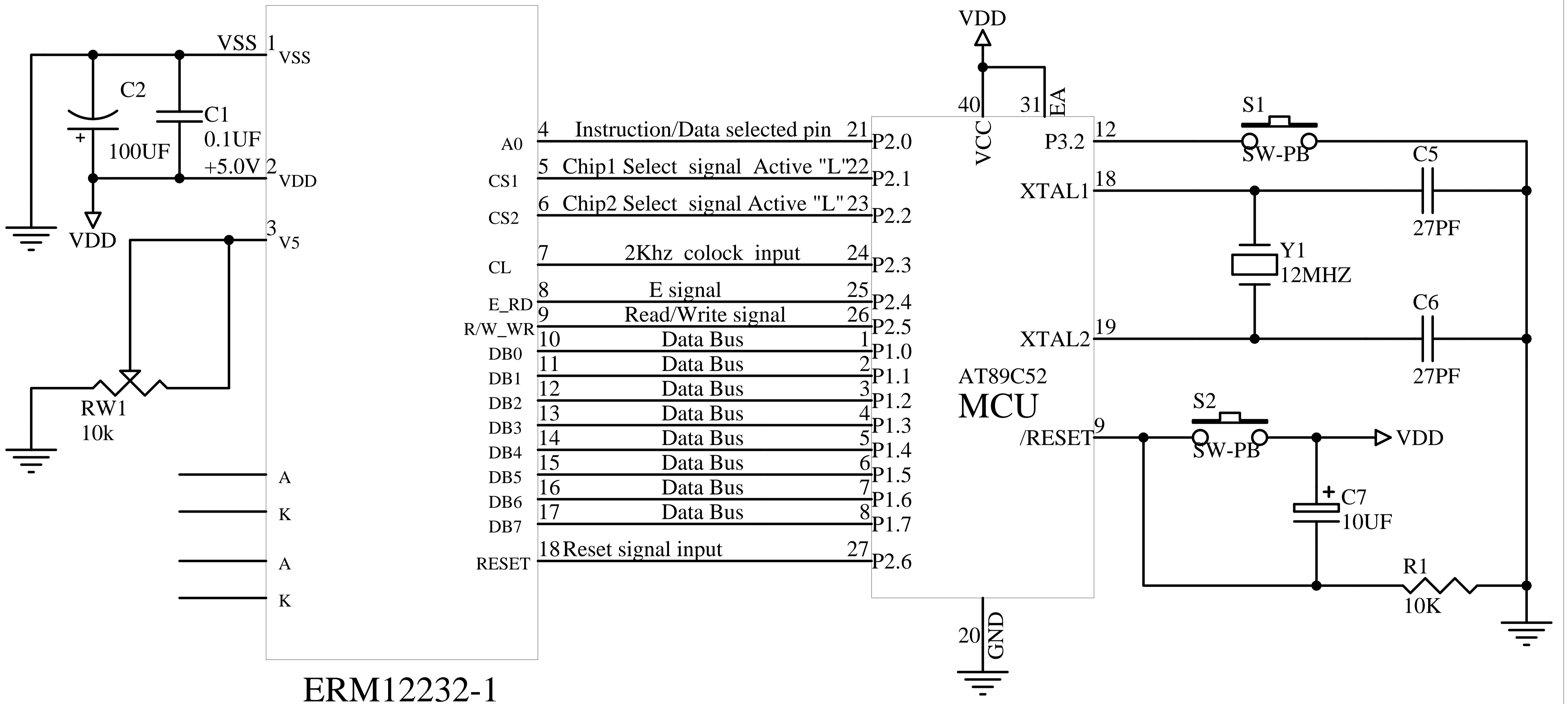
Interfacing LCD Modules to the AT89C52 MCU

The Parallel 8Bit(8080 Series MCU)Reference Example



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The Parallel 8Bit(6800 Series MCU)Reference Example



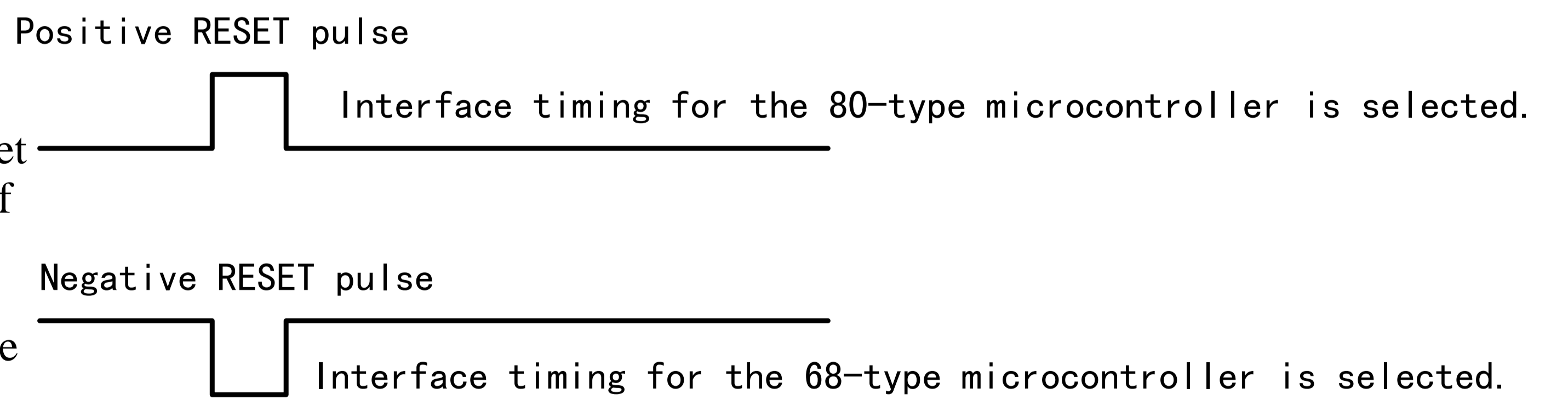
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Hardware RESET and interface type selection.

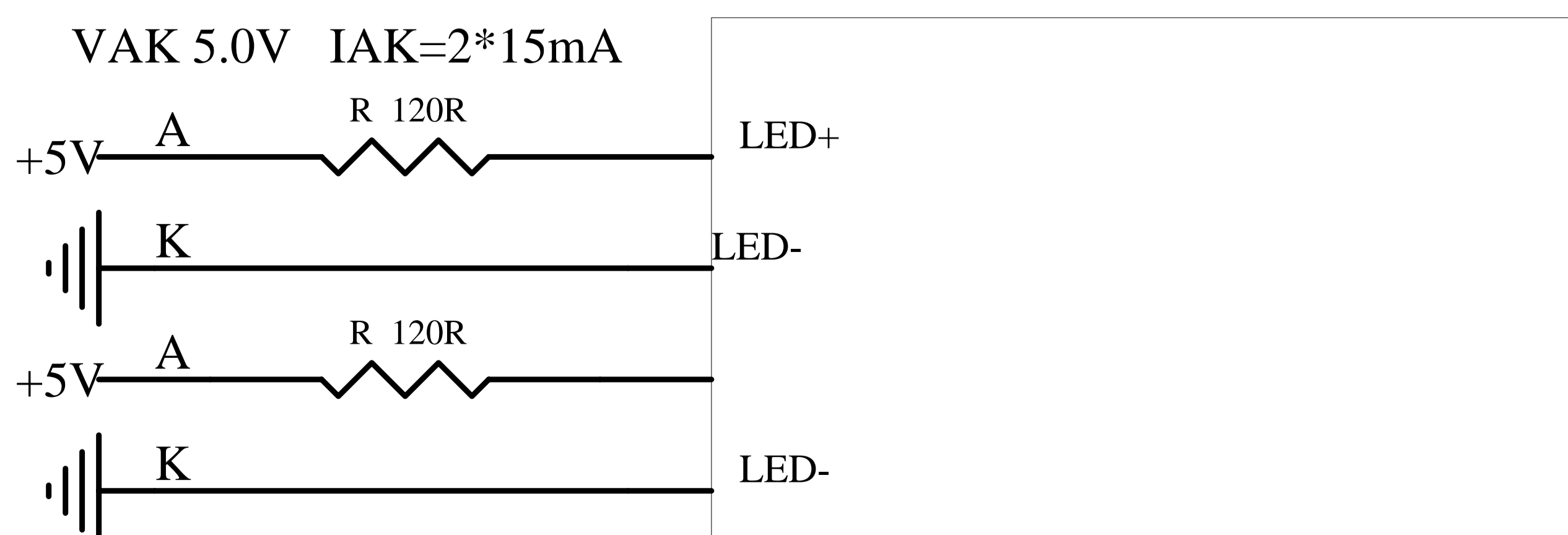
This pin is a dual function pin. It can be used to reset the SBN1661G_X and select the type of interface timing.

The hardware RESET is edge-sensitive. It is not level-sensitive. That is, either a falling edge or a rising edge on this pin can reset the chip. The voltage level after the reset pulse selects the type of interface timing. If the voltage level after the reset pulse stays at HIGH, interface timing for the 68-type microcontroller is selected. If the voltage level after the reset pulse stays at LOW, then interface timing for the 80-type microcontroller is selected.

Therefore, a positive RESET pulse selects the 80-type microcontroller for interface and a negative RESET pulse selects the 68-type microcontroller for interface.



LED Backlight with white



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