



PCB HOLES(PATTERN SIDE) t=1.60mm
 NOTE:GENERAL TOLERANCE:±0.1mm

8.	DAT1	I/O/PP	DATE LINE(BIT1)
7.	DAT0	I/O/PP	DATE LINE(BIT0)
6.	VSS	S	SUPPLY VOLTAGE GROU
5.	CLX	I	CLOCK
4.	VDD	S	SUPPLY VOLTAGE
3.	CMD	PP	COMMAND RESPONSE
2.	CD/DAT3	I/O/PP	CARD DETECT DATE LINE(BIT3)
1.	DAT2	I/O/PP	DATE LINE(BIT2)
NO.	NAME	TYPE	DESCRPTION

TOLERANCE UNLESS OTHERWISE SPECIFIED				
.X±0.25	X:± 5'	APR.	TITLE: TF 9P CARD	UNITS. MM
.XX±0.15	.X'± 3'	CHK.	DWG NO.	SHEET. 1 / 1
.XXX±0.10	.XX'± 1'	DRA. Jesson	PROJ.	CUSTOMER DRAWING
			SCALE: 1:1	REV. A0

A0	FIRST RELEASE	2012.03.28
REV.	Q'TY	ECN. NO.
	APR.	DATE